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Significant Suppression of Surface Leakage in GaSb/AlAsSb Heterostructure with Al₂O₃ Passivation

Andrew Chen¹, Bor-Chau Juang¹, Dingkun Ren¹, Baolai Liang^{2*}, David L. Prout³, Arion F. Chatziioannou³, and Diana L. Huffaker¹

¹Department of Electrical and Computer Engineering, University of California - Los Angeles, CA 90095, USA.

²California NanoSystems Institute, University of California - Los Angeles, CA 90095, USA

³Department of Medical and Molecular Pharmacology, University of California - Los Angeles, CA 90095, USA

* E-mail: bliang@cnsi.ucla.edu

This work develops a (NH₄)₂S/Al₂O₃ passivation technique for photodiode-based GaSb/AlAsSb heterostructure. Surface-sulfurated GaSb/AlAsSb heterostructure mesas show a significant suppression of reversed-bias dark current by 4 – 5 orders of magnitude after they are further passivated by Al₂O₃ layers. So the mesa sidewalls treated with (NH₄)₂S/Al₂O₃ layers can effectively inhibit the shunt path of dark carriers. The activation energies for both bulk and surface components are extracted from temperature-dependent current-voltage characteristics, which suggest that the bulk characteristics remain unchanged, while Fermi-level pinning at surfaces is alleviated. Additionally, temperature coefficients of the breakdown voltage are extracted, confirming that the breakdown process is confined entirely in the large bandgap AlAsSb regions. This study shows that the implementation of (NH₄)₂S/Al₂O₃ passivation can lead to room-temperature GaSb-based photodiodes and GaSb/AlAsSb-based avalanche photodiodes for highly efficient photodetection.

Gallium antimonide (GaSb), a compound III-V semiconductor material with bandgap of 0.726 eV (or equivalent absorption wavelength $\sim 1.71 \mu\text{m}$) at room temperature, has been implemented into a variety of photodetectors at short-, mid-, and long-wavelength infrared regimes.¹⁻⁵⁾ To fabricate pixels, i.e., focal plane arrays, bulk GaSb materials are typically etched by chemicals into mesa shapes with exposed GaSb surfaces (or GaSb/air interfaces). It is known that nonradiative recombination centers on surfaces, i.e., surface states (or dangling bonds), which are caused by interruptions to the crystal periodicity, can lead to high leakage current at reverse bias and thus poor signal-to-noise ratio. To minimize this effect, it is critical to properly passivate surfaces using chemicals or dielectric layers to prevent sidewalls from being re-oxidized. Unfortunately, many studies have shown that improving surface quality for antimony-based (Sb-based) material systems is challenging due to the complex combination of native oxides on their surfaces.⁶⁻⁹⁾ This is because the exposed mesa sidewall tends to form a thin layer of native oxide (e.g. Ga_2O_3 and Sb_2O_3 through $2\text{GaSb} + 3\text{O}_2 \rightarrow \text{Ga}_2\text{O}_3 + \text{Sb}_2\text{O}_3$), and a layer of elemental antimony (Sb) is normally observed at the interface between bulk GaSb and its native oxide, resulting in a shunt path for carriers.¹⁰⁾ Thus, a crucial step in the fabrication is to develop a robust and controllable passivation technique for the etched GaSb surfaces.

To date, a wide range of surface passivation methods (i.e., ammonium sulfide $(\text{NH}_4)_2\text{S}$,¹¹⁻¹³⁾ SiO_2 ,^{15,16)} SiN_x ,¹⁶⁾ polyamide,¹⁷⁾ and overgrowth with large bandgap material¹⁸⁾) have been reported for fabrication of Sb containing photodetectors. Among these techniques, sulfuration and SiO_2 deposition are the most common. $(\text{NH}_4)_2\text{S}$ solution (with varied dilution ratios) can etch off the native oxides and saturate dangling bonds. In our study, however, we observed that the dark current density of GaSb-based detectors after $(\text{NH}_4)_2\text{S}$ treatment remains relatively high at room temperature ($\sim 6 \times 10^{-4} \text{ A/cm}^2$ at reverse bias of 5 V) and increases with decreasing mesa size, which suggests that surface leakage current is still prominent.¹⁹⁾ Furthermore, the treated GaSb surfaces tend to degrade over time as the sulfur atoms gradually desorb.^{14,19)} These observations suggest that the $(\text{NH}_4)_2\text{S}$ passivation technique is unstable and needs to be further developed. On the other hand, passivation using SiO_2 is promising because the layer thickness can be freely controlled by the deposition time while such a dielectric layer can prevent the desorption of sulfur. However, the SiO_2 deposition process normally requires high temperature (or high radio-frequency power) to excite plasma, which can damage the material surface quality and introduce additional dangling bonds or surface states.

To overcome these issues, we optimize the surface passivation for GaSb using a

combination of $(\text{NH}_4)_2\text{S}$ and conformal Al_2O_3 layer by atomic-layer deposition (ALD). The ALD technique is plasma-free and is conducted at low temperature ($\sim 200^\circ\text{C}$). Moreover, it has been discovered that Al_2O_3 can suppress the formation of native oxide on III-V material surfaces during deposition due to lower Gibbs free energy (-377.9 kcal/mol).^{13,20)} This value is lower than the Gibbs free energies of Sb_2O_3 , Ga_2O , Ga_2O_3 , As_2O_3 and As_2O_5 , which are -151.5 kcal/mol , -75.3 kcal/mol , -238.6 kcal/mol , -137.7 kcal/mol and -187.0 kcal/mol , respectively.^{13,21)} In addition, the electrical characterization of GaSb-based metal–oxide–semiconductor capacitors has showcased successful elimination of Sb_2O_3 after Al_2O_3 deposition.^{13,22)} Here we introduce a sulfurized surface with Al_2O_3 passivation to our GaSb/AlAsSb mesa heterostructures. Such heterostructures are based on our previously studied GaSb/AlAsSb separate absorption and multiplication avalanche photodiodes (SAM-APDs).²³⁾ We observe a significant suppression of dark current at reverse bias, and a remarkable improvement of avalanche breakdown, both at room temperature. To understand the underlying physics, we further extract activation energies and breakdown temperature coefficients. We observe a remarkable difference in surface-related activation energies from devices with and without Al_2O_3 , while the activation energies from bulk materials remain almost constant. This clearly suggests that the surface passivation plays an important role in modulating surface quality and device characteristics. This study paves the way to room-temperature GaSb-based photodiodes and GaSb/AlAsSb-based avalanche photodiodes for highly efficient photodetection.

Figure 1 shows a schematic of the GaSb/AlAsSb heterostructure with Al_2O_3 passivation, which is composed of both small bandgap GaSb and large bandgap $\text{AlAs}_{0.08}\text{Sb}_{0.92}$ PIN regions. Such heterostructures were developed based on our previous study on GaSb PIN diodes and GaSb/AlAsSb SAM-APDs for energy-sensitive photodetection, along with a series of detector characterizations, which include current-voltage (I-V), capacitance-voltage (C-V), photo-response, radiation response, among others.^{19,23-25)} The AlAsSb region was lattice-matched to the GaSb region, grown by molecular beam epitaxy (MBE) on an *n*-type GaSb substrate.^{23,25)} Tellurium and Beryllium were used as *n*-type and *p*-type dopants, respectively. The growth was started with the multiplication region. An $\text{AlAs}_{0.08}\text{Sb}_{0.92}$ PIN junction (digital-alloy) with a *p*-AlAsSb field-control layer ($1 \times 10^{17}\text{ cm}^{-3}$) was first formed, followed by the growth of a $2\text{ }\mu\text{m}$ un-doped GaSb absorber. The GaSb absorption region was capped by a 50 nm large bandgap *p*- $\text{Al}_{0.4}\text{Ga}_{0.6}\text{Sb}$ window layer ($7 \times 10^{18}\text{ cm}^{-3}$), followed by growth of 50 nm highly-doped *p*-GaSb atop as ohmic contact ($1 \times 10^{18}\text{ cm}^{-3}$). In this GaSb/AlAsSb heterostructure, the majority of the electric field can be tightly confined within the

large bandgap AlAsSb multiplication region, which can minimize the dark current from Shockley–Read–Hall nonradiative recombination and band-to-band tunneling.

After the MBE growth, the devices were fabricated into circular mesas of five sizes ($d = 25 \mu\text{m}$, $50 \mu\text{m}$, $100 \mu\text{m}$, $200 \mu\text{m}$, and $400 \mu\text{m}$) by standard photolithography using inductively coupled plasma etcher (BCl_3/Ar). The etching method was chosen by the fact that it gives the lowest possible dark current floor among the available approaches. Then, the samples were submerged in a $(\text{NH}_4)_2\text{S}$ solution for 15 minutes, i.e., sulfuration, as the first step of surface passivation. The dilution ratio was $(\text{NH}_4)_2\text{S}:\text{H}_2\text{O} = 1:4$. Next, the samples were blow-dried by nitrogen (without rinsing with deionized water) and quickly transferred into the ALD system (Cambridge Nanotech Fiji F200). A $\sim 30 \text{ nm}$ thick thermal Al_2O_3 layer (300 cycles with $\sim 1 \text{ \AA/cycle}$) was deposited at 200°C (substrate temperature). During deposition, 0.06 sccm trimethylaluminum (TMA) and 0.06 sccm water vapor (H_2O) pulses were delivered into the chamber in a sequential fashion under a constant nitrogen flow of 20 sccm . To take away any by-products, a wait time of 10 sec was added between two neighboring cycles. Note that both TMA and H_2O were unheated. Finally, the Al_2O_3 -passivated devices were etched by buffered oxide etch (BOE) to expose both top and bottom contacts. The etch rate on Al_2O_3 was calibrated to 1 nm/sec . Using BOE instead of concentrated hydrofluoric (HF) acid (49%) can result in better etch profiles since concentrated HF is corrosive to photoresist.²⁶⁾

The Al_2O_3 ALD deposition mechanism has been previously studied.^{27,28)} During sequential deposition cycles, the TMA and H_2O precursors react with the gas molecules, which are adsorbed on the device surfaces at the previous stage. Compared with other dielectric deposition techniques, ALD can achieve higher-quality GaSb surfaces due to several reasons, for example: (1) a precise control of deposition thickness at atomic level (since the thickness only depends on the number of cycles), (2) ideal conformal coverage, and (3) large area thickness uniformity. Equipped with these properties and the fact that Al_2O_3 formation is energetically favorable for GaSb over other dielectrics, Al_2O_3 by ALD deposition was the preferred option.

It is extremely critical to cap sulfured GaSb/AlAsSb surfaces by Al_2O_3 layer to achieve optimized surface quality. We surmise that sulfur treatment followed by Al_2O_3 capping changed the dielectric environment, as in the case of passivation for III-AsSb.^{29,30)} The sulfur etched the native Al, Ga, As, and Sb oxides on the surface and passivated the dangling bonds.³¹⁻³³⁾ Any remaining native oxides formed following the sulfur treatment or sulfur desorption can be further etched by the ALD precursor (i.e., TMA). As a result, we

hypothesize two possible reasons why Al_2O_3 improves passivation: (1) Al-O participates in interfacial bonding through supplementing or substituting sulfur terminated bonds,³³⁾ and (2) Al_2O_3 shells prevent desorption of sulfur atoms from the detector surface.

The room-temperature I-V characteristics at different passivation stages are shown in Figure 2(a) with a constant mesa diameter d of 200 μm . The $(\text{NH}_4)_2\text{S}/\text{Al}_2\text{O}_3$ passivation results an over 4-order reduction of dark current compared with that of the as-fab device (without any passivation), while the passivation with only $(\text{NH}_4)_2\text{S}$ treatment gives about 3-order reduction. Interestingly, electrical breakdown is only observed at room temperature for the case of $(\text{NH}_4)_2\text{S}/\text{Al}_2\text{O}_3$. This suggests that Al_2O_3 layers offer good insulation and lower dark current floor by suppressing the surface leakage current. We further examine the dark current density of the Al_2O_3 -passivated devices with varied d , as shown in Figure 2(b). Clearly, the dark current density increases with decreasing d below the breakdown voltage (V_{BD}), which means that at high reverse bias the surface leakage current remains significant regardless of surface passivation.

The temperature-dependent I-V characteristics are further studied. We first select two devices with d of 100 μm and 200 μm and investigate the impact of surface passivation on their reverse leakage current at liquid-nitrogen cooled temperature, i.e., 77 K. As shown in Figure 3, capping Al_2O_3 layers on sulfurated GaSb surfaces offers an additional reduction of leakage current by 2 – 3 orders of magnitude around V_{BD} (~ 27 V). It should be noted that V_{BD} does not show a significant variation, which indicates that the bulk material properties remain the same regardless of passivation with or without Al_2O_3 layers. The noise level of the I-V measurement setup is on the order of 10^{-13} A.

As shown in Figure 4(a), a strong dependence of the dark current on temperature is observed with no abrupt increase before breakdown, which indicates that band-to-band-tunneling (BTBT) current from both AlAsSb and GaSb regions is negligible. To analyze the activation energy, the total dark current density (I_{total}) is decoupled into two components, and can be described as

$$J_{\text{total}} = J_{\text{bulk}} + J_{\text{surface}} \times \frac{P}{A} \quad (1)$$

where J_{bulk} (A/cm^2) is the current density from the bulk material, J_{surface} (A/cm) is the current density from the surface leakage, A (cm^2) is the device area (i.e., mesa size), and P (cm) is the mesa perimeter. With the decoupled J_{bulk} and J_{surface} at different temperatures (77 K – 320 K) at a given bias (-0.1 V is chosen here for minimized field effect), their respective activation energies can be fitted using the following Arrhenius model:

$$J \propto T^{\frac{3}{2}} \times \exp\left(\frac{-E_a}{kT}\right) \quad (2)$$

where T is temperature, k is the Boltzmann constant and E_a is the activation energy. Figure 4(b) shows the Arrhenius plots of bulk current from GaSb devices with and without Al_2O_3 passivation. The extracted energies are 0.297 eV and 0.304 eV, respectively. The nearly identical activation energies indicate that there is no degradation of the bulk materials after Al_2O_3 passivation. In contrast, the Al_2O_3 -passivated device shows a surface activation energy of 0.178 eV, about 30% higher than that of the device with only $(\text{NH}_4)_2\text{S}$ passivation (0.132 eV) as shown in Figure 4(c). This result suggests that surface conduction is significantly suppressed and Fermi-level pinning is effectively alleviated in the Al_2O_3 -passivated devices.

Last but not least, the temperature coefficients of breakdown voltage (C_{bd}) are extracted for the GaSb/AlAsSb heterostructures with and without Al_2O_3 passivation, yielding 17.4 ± 0.5 mV/K and 16.51 ± 1.9 mV/K, respectively. Positive value of C_{bd} suggests that the dominant breakdown mechanism is the avalanche process. No significant increment of dark current is observed before the breakdown (Figure 2 (b)), suggesting the breakdown process takes place only in the AlAsSb PIN regions. This observation agrees with the result from our previously shown GaSb/AlGaSb SAM-APDs, i.e., the electric field is entirely confined in the large bandgap AlAsSb regions thanks to the SAM structure.²³⁾ Conversely, if the breakdown event takes place in the small bandgap GaSb regions, an abrupt increment in dark current from BTBT would occur. In addition, the comparable C_{bd} regardless of the passivation treatment is likely due to the fact that C_{bd} is an intrinsic bulk characteristic and not affected by the surface conditions.

In conclusion, a $(\text{NH}_4)_2\text{S}/\text{Al}_2\text{O}_3$ passivation technique is developed and evaluated for the GaSb/AlAsSb heterostructures with different mesa sizes. We observed a significant suppression of dark current at reverse bias as well as an improved avalanche breakdown at room temperature. The activation energies were extracted from temperature-dependent I-V characteristics for both bulk and surface components. The results suggest that the bulk characteristics were not affected by the passivation process, while the surface conduction was significantly suppressed and Fermi-level pinning was effectively alleviated after the passivation process. Additionally, temperature coefficients of breakdown voltage were studied, which shows the breakdown process was confined entirely in the multiplication regions thanks to the SAM structure. This work provides an effective passivation technique to achieve room-temperature GaSb-based photodiodes and SAM-APDs for high-efficiency photodetection with significant applications in both industrial and research settings.

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Figure Captions

FIG. 1. Schematic of GaSb/AlAsSb heterostructure mesa passivated with a 30 nm Al₂O₃ film.

FIG. 2. Electrical characterizations at room temperature (295 K). (a) I-V characteristics of the GaSb/AlAsSb device at different stages of passivation; (b) J-V characteristics of the Al₂O₃-passivated GaSb/AlAsSb devices with different mesa sizes.

FIG. 3. Electrical characterizations at low temperature (77 K). I-V characteristics of passivated and unpassivated GaSb/AlAsSb devices with two different mesa sizes of 100 μm and 200 μm.

FIG. 4. Study of activation energies. (a) Temperature-dependent I-V characteristics of the Al₂O₃-passivated GaSb/AlAsSb device with mesa size of 200 μm; (b) Arrhenius plots for dark current contributed by the bulk materials; (c) Arrhenius plots for dark current contributed by the surface leakage.

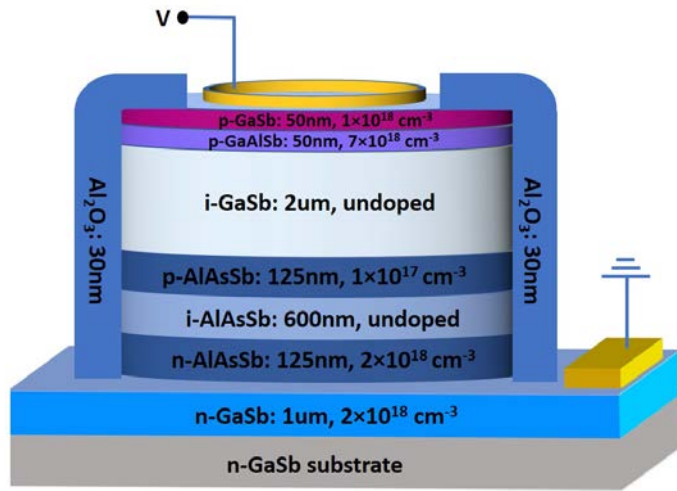
Fig. 1

Fig. 2

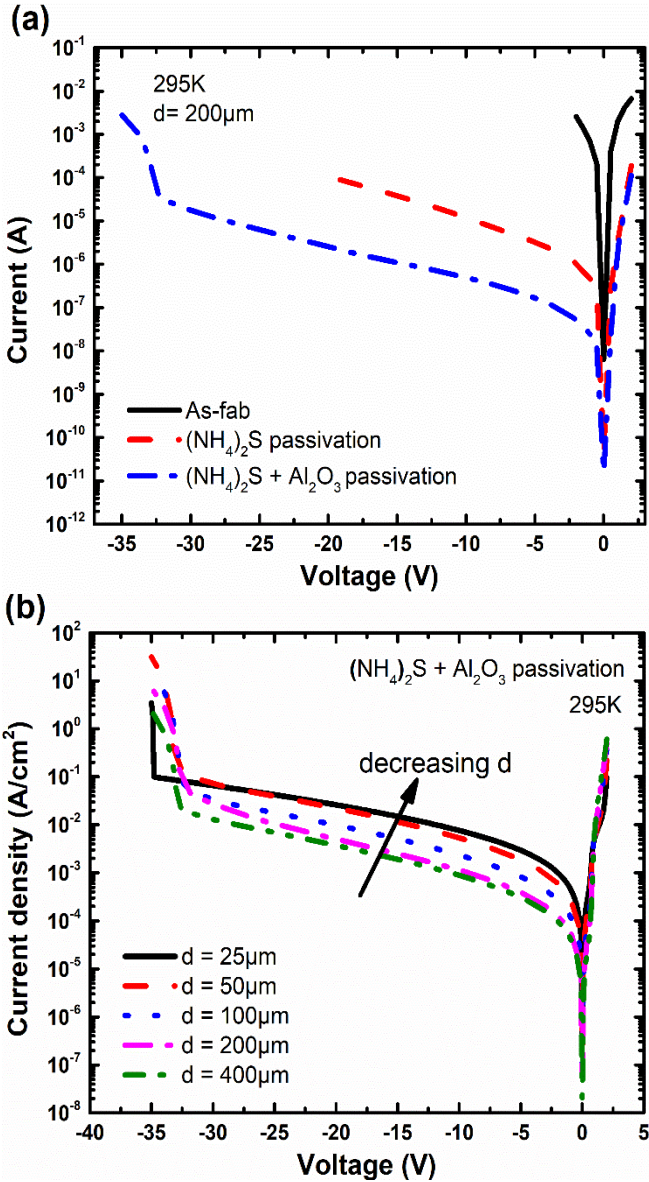


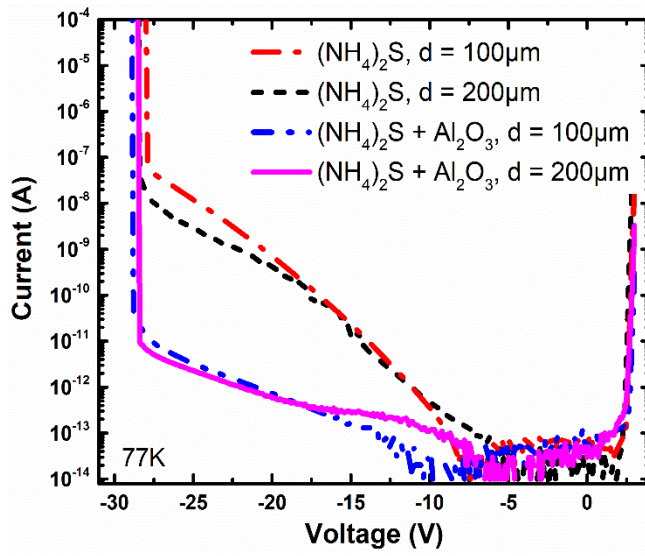
Fig. 3

Figure 4

