With wide applications of power electronic devices in modern power systems, simulation using traditional electromechanical and electromagnetic tools suffers low speed and imprecision. Multi-rate methods can enhance efficiency of simulation by decreasing the scale of systems in small time-steps. However, the existing traditional methods for multi-rate simulation suffer the problems of instability and simulation errors. These have hindered the application of multi-rate simulation in power industry. Therefore theoretical evaluation on different multi-rate simulation methods is crucial to understand the feasibility and limitation of the methods, and to contribute to overcome the drawbacks of the traditional methods. In this paper, the multi-rate simulation performance based on two traditional technologies and a Modified Thevenin Interface are evaluated to provide an overall feasibility of multi-rate algorithms in the power simulation. The Modified Thevenin Interface is proposed to overcome the drawbacks in synchronization. Three theorems are proposed and proved for theoretically analyzing the stability of the simulation methods. Error analyses of the multi-rate methods are performed to identify the relationships between errors and simulation conditions. Besides, the accuracy and efficiency performance in a practical project of VSC-MTDC shows the feasibility and necessity by using multi-rate simulation. Through the theoretical analysis, the issues of stability and accuracy of multi-rate simulation for the DC grids have been better understood, based on which an improved simulation algorithm has been proposed to overcome these issues. Long-term system dynamics of large-scale systems containing DC grids and fast transients of HVDC converters can be investigated simultaneously with high speed and sufficient accuracy.

Keywords
Electromagnetic transient analysis, Power System Simulation, Parallel Algorithm, Multi-rate Interface, Different Rates, Multi-terminal VSC-HVDC

Submission Files Included in this PDF
File Name [File Type]
cover_letter V2.doc [Cover Letter]
Review_IndV10.docx [Response to Reviewers]
multirate simulation for DC grid0508.docx [Manuscript File]
Highlights.doc [Highlights]

To view all the submission files, including those not included in the PDF, click on the manuscript title on your EVISE Homepage, then click 'Download zip file'.
Manuscript title:
Systematic Evaluation for Multi-Rate Simulation of DC Grids

Short running title:
Evaluation for Multi-Rate Simulation

What does the current manuscript add to the existing knowledge? (Research Highlights)

1- Three theorems for the stability of three typical multi-rate simulation algorithms have been proposed and proved.
2- The error of three typical multi-rate simulation algorithms has been modelled.
3- The backward synchronization for the Thevenin equivalent interface has been proposed to overcome the challenge of synchronization.
4- Three typical multi-rate simulation algorithms have been compared to illustrate that the modified Thevenin equivalent interface is the most stable and accurate.
5- The multi-rate simulation algorithm has been used in the studies of DC grids, which achieve the great improvement of the simulation speed.

We affirm that the submission represents original work that has not been published previously and is not currently being considered or submitted to another journal, until a decision has been made. Also, we confirm that each author has seen and approved the contents of the submitted manuscript.

Signature (on behalf of all co-authors (if any))

Corresponding author
Name: Qing Mu
Affiliation: Cardiff University
Tel.: +8618612425770
Fax:
E-mail address: muqing_zju@163.com

Submission date: 24th, Aug
Responses:

The authors would like to thank the reviewers for their constructive comments. The responses to each comment have been carefully prepared by the authors and are given as below. Corresponding modifications of the paper have also been indicated.

**Reviewer 1:**

The abstract can be revised to include more details of the proposed approach. Authors can improve the abstract by including the existing challenges, motivations and outcomes of the paper. Furthermore, it is suggested that authors add 1-2 sentences to motivate this study before stating the novelty of their work.

The abstract has been revised according to the suggestions.

"However, the existing traditional methods for multi-rate simulation suffer the problems of instability and simulation errors. These have hindered the application of multi-rate simulation in power industry. Therefore theoretical evaluation on different multi-rate simulation methods is crucial to understand the feasibility and limitation of the methods, and to contribute to overcome the drawbacks of the traditional methods."

The outcomes are included in Abstract.

"Through the theoretical analysis, the issues of stability and accuracy of multi-rate simulation for the DC grids have been better understood, based on which an improved simulation algorithm has been proposed to overcome these issues. Long-term system dynamics of large-scale systems containing DC grids and fast transients of HVDC converters can be investigated simultaneously with high speed and sufficient accuracy."

Introduction section is incomplete. It should include four key components: 1.1.motivation, 1.2. literature survey, 1.3. contributions, and 1.4 the structure of paper. Please modify this section accordingly.

The structure of the Introduction has been revised according to your constructive suggestions. Please refer to the paper for the detailed content.

Authors are encouraged to add nomenclature in the first section to include are parameters, variables, and abbreviations.

The nomenclature has been added in Page 2 and Page3 according to your constructive suggestions. Please refer to the paper for the detailed content.

Please add a list of key contributions of this study in the newly-added subsection 1.3. It is not clear for the readers whether the authors proposed some theorems or they just applied some available methods to a system?

The key contributions have been listed in 1.3 Contribution of the Introduction, in Line 9, Page 5, according to your supportive suggests.

"This paper is to provide the systematic evaluation of the multi-rate simulation methods and investigate the stability and accuracy. The major contributions are listed as below:
1. An improved multi-rate method based on the Thevenin equivalent circuit, named Modified Thevenin Equivalent Interface (MTI), is proposed to overcome the issues of synchronization in multi-rate simulation.

2. Three theorems have been proposed and proved to illustrate the stability characteristics of ITI, TLI and MTI.

3. The simulation errors using ITI, TLI and MTI have been analytically evaluated and the factors of these errors have been obtained.

4. The systematical analysis and comparisons of ITI, TLI and MTI have been performed in terms of stability and accuracy.

Authors are encouraged to illustrate the results of table I using some plots to make it easy to understand.

The Table I has been replaced by a clear figure, as Fig.18 in Page 19, according to your constructive suggestions.

![Fig. 1 Computation Time Comparison of different subsystem division types.](image)

Furthermore, the figure has been explained in Line 10 from bottom, Page 18.

“The detailed comparison of different dividing schemes is illustrated in Fig. 18. The label in the axis of X represents the division type, (number of AC subsystems + number of DC subsystems). The PSCAD software runs about 1860s for 1s period of simulation as the computation time base.”

According to theorem 2” If the simulation of TLI with a single rate is stable, and the divided fast and slow subsystems are also absolutely stable, then the multi-rate simulation algorithm without interpolation is stable as long as all the multiple rates are not larger than the single rate.”. Please provide thorough explanations about the assumptions of this theorem.

The clear explanation of the assumptions is added in Line 6 from bottom, Page 11.

“The assumption of this theorem includes that:

- A system is divided into subsystems;
- Stable simulation can be achieved for the subsystems connected through TLI interface using the same time-steps for all subsystems, called a single rate, H. This means that the state transfer matrix in an equation, similar to (21), has the norms less than 1.
- Each subsystem is asymptotically stable, which means that the A11 and A22 in (30) have norms less than 1.
- Different simulation time-steps, which are not larger than the single rate H, are used for
subsystems. Fast subsystems have small time-steps and slow subsystems have relatively large time-steps.

When the above assumptions are met, stable simulation can be achieved for the fast and slow subsystems connected through TLI.”

In addition, the word “absolutely” in Theorems 1, 2 and 3 is not accurate enough, the “asymptotically” is more accurate. Therefore, the “absolutely” has been replaced with “asymptotically” in the three theorems.

Quality of figures 8, 10, 11, 12, 13, 14, and 15 requires improvement. The font size should be justified with the text and clear to read.

Figures 6, 7, 8, 10, 11, 12, 13, 14, 15, 16, 19 and 20 are redrawn in the paper to improve the figure quality.

In order to make the conclusion section more clear, authors need to include the point-by-point findings of this article. The current conclusion is written very wide and it is not easy to maintain the key findings.

The conclusion has been revised and the key findings have been listed.

“ The key findings of the study include
1. An improved method, Modified Thevenin Interface (MTI) is proposed to overcome the drawbacks in synchronization of the original Thevenin Interface.
2. Three theorems about ITI, TLI and MTI are proposed and proved in theoretically. From these theorems, it has been found that the stability of multi-rate simulation using these interfaces is only associated with the stability of single rate parallel simulation using these interfaces. The stability of MTI is only associated with the stability of the simulated case. Therefore, MTI performs higher stability and less limitation.
3. In terms of error analysis, the errors of ITI only depend on the delay introduced by the parallel algorithm, which are smaller than the errors of TLI.
4. The errors of TLI are dependent on the associated capacitor of TLI and the equivalent admittance of the subsystem from the interface.
5. The errors of the MTI are convergent and much smaller than the previous two.

Through the proposed theorems and methods, the accuracy and stability of the multi-rate parallel simulation of DC grids are able to be evaluated conveniently. Multi-rate simulation has been performed to analyze the practical project, Zhoushan MTDC in China, which presents high speed and accuracy. The suitable multi-rate simulation algorithm can be applied in the analysis of DC grids to achieve fast and accurate simulation results.”

The literature survey (subsection 2 in the revised introduction section) is incomplete. It should include related articles about DC power system analysis methods [1][2][3], parallel/distributed methods to deal with DC power networks [4], and also the stability analysis of large scale power systems [5] Authors need to review the mentioned articles carefully in the literature survey section. [1]. "Determination of the minimum-variance unbiased estimator for DC power-flow estimation." IECON 2014-40th Annual Conference of the IEEE Industrial Electronics Society. IEEE, 2014. [2]. "Error Detection of DC Power Flow Using State Estimation." Smart Grids: Security and Privacy Issues. Springer
According to your constructive suggestion, the section of Literature review has been revised. References of the DC power flow, stability analysis of large-scale power system containing DC grids, the parallel DC network control have been included in the section 1.2 Literature review.

“DC grids, as an emerging technology, attract extensive studies. In [13], two multi-terminal DC modelling are studied in DC power flow using the Newton method. However, the operation modes of DC converters are too simple to present the practical situation. In [14], more general and detailed multi-terminal DC modelling in the sequential AC/DC power flow algorithm is proposed considering the power losses of the converters. The DC power flow method is simpler and faster than the Newton method. In [15, 16], a state estimation approach for linear problems is utilized in order to estimate the DC power flow. In [17], to remove the measure error, the sparse-based error detection technology is used to obtain the accurate DC power flow results. To manage DC network power, the distributed power dispatch method is proposed to replace the original centralized manner, which can greatly reduce the computation burden [18].

The transient study is another important aspect for large-scale power system containing DC grids. [19] proposed the MTDC modeling including DC lines, converters and DC controllers for transient study. In [20], the saturation of a generator is considered in the transient studies to investigate the bifurcation issues.

Electromagnetic simulation is still served as a major method for DC grid analysis [21]. The transient issues in DC grids, such as the DC controllers and IGBTs, belong to small time-scales, which can only be simulated using electromagnetic programs. Multi-rate parallel simulation is a suitable technology to analyze the DC grids and the interaction between a DC grid and a large-scale power system. The singular perturbation method is used to solve fast-slow systems [22]. However this method is not suitable for power electronics devices of which both fast and slow dynamics must be investigated, because this method uses asymptotic solution which neglects fast transients of the systems.

In order to investigate the fast transient of DC grids, there are three major algorithms to perform the parallel computation.”

The following references are added:


Abstract—With wide applications of power electronic devices in modern power systems, simulation using traditional electromechanical and electromagnetic tools suffers low speed and imprecision. Multi-rate methods can enhance efficiency of simulation by decreasing the scale of systems in small time-steps. However, the existing traditional methods for multi-rate simulation suffer the problems of instability and simulation errors. These have hindered the application of multi-rate simulation in power industry. Therefore theoretical evaluation on different multi-rate simulation methods is crucial to understand the feasibility and limitation of the methods, and to contribute to overcome the drawbacks of the traditional methods. In this paper, the multi-rate simulation performance based on two traditional technologies and a Modified Thevenin Interface are evaluated to provide an overall feasibility of multi-rate algorithms in the power simulation. The Modified Thevenin Interface is proposed to overcome the drawbacks in synchronization, Three theorems are proposed and proved for theoretically analyzing the stability of the simulation methods. Error analyses of the multi-rate methods are performed to identify the relationships between errors and simulation conditions. Besides, the accuracy and efficiency performance in a practical project of VSC-MTDC shows the feasibility and necessity by using multi-rate simulation. Through the theoretical analysis, the issues of stability and accuracy of multi-rate simulation for the DC grids have been better understood, based on which an improved simulation algorithm has been proposed to overcome these issues. Long-term system dynamics of large-scale systems containing DC grids and fast transients of HVDC converters can be investigated simultaneously with high speed and sufficient accuracy.

Index Terms—Electromagnetic transient analysis, Power System Simulation, Parallel Algorithm, Multi-rate Interface, Different Rates, Multi-terminal VSC-HVDC

The research leading to these results has received funding from the People Programme (Marie Curie Actions) of the European Union’s Seventh Framework Programme FP7/2007-2013/ under REA grant agreement no. 317221, project title MEDOW, and from the Project supported by the National Science Foundation for Distinguished Young Scholars of China (Grant No. 51407164).
<table>
<thead>
<tr>
<th><strong>NOMENCLATURE</strong></th>
</tr>
</thead>
<tbody>
<tr>
<td>$A_{fs}$</td>
</tr>
<tr>
<td>$Acc$</td>
</tr>
<tr>
<td>$B_{fs}$</td>
</tr>
<tr>
<td>$C$</td>
</tr>
<tr>
<td>$C_{fs}$</td>
</tr>
<tr>
<td>$C_{TLI}$</td>
</tr>
<tr>
<td>$D_{fs}$</td>
</tr>
<tr>
<td>$diag[0, I_{n_1}]$</td>
</tr>
<tr>
<td>$e$</td>
</tr>
<tr>
<td>$e_{sf}$</td>
</tr>
<tr>
<td>$F$</td>
</tr>
<tr>
<td>$g()$</td>
</tr>
<tr>
<td>$G_{f}(s)$</td>
</tr>
<tr>
<td>$g_{s}()$</td>
</tr>
<tr>
<td>$G_{s}(s)$</td>
</tr>
<tr>
<td>$h$</td>
</tr>
<tr>
<td>$H$</td>
</tr>
<tr>
<td>$I_{f}$</td>
</tr>
<tr>
<td>$i_{f}$</td>
</tr>
<tr>
<td>$I_{s}$</td>
</tr>
<tr>
<td>$i_{s}$</td>
</tr>
<tr>
<td>$ITI$</td>
</tr>
<tr>
<td>$k$</td>
</tr>
<tr>
<td>$K$</td>
</tr>
<tr>
<td>$L_{1}$</td>
</tr>
<tr>
<td>$L_{2}$</td>
</tr>
<tr>
<td>$m$</td>
</tr>
<tr>
<td>$MTI$</td>
</tr>
<tr>
<td>$R(m)$</td>
</tr>
<tr>
<td>$T$</td>
</tr>
<tr>
<td>$t$</td>
</tr>
<tr>
<td>$TLI$</td>
</tr>
<tr>
<td>$U_{fAA'}$</td>
</tr>
<tr>
<td>$U_{fint}$</td>
</tr>
<tr>
<td>$U_{sAA'}$</td>
</tr>
<tr>
<td>$U_{sint}$</td>
</tr>
</tbody>
</table>
modelling of slow subsystem

\( V_{eq}(s) \) Transfer function of Thevinen equivalent voltage sources of the fast subsystem

\( V_{eq}(s) \) Transfer function of Thevinen equivalent voltage sources of the slow subsystem

\( V_f \) Voltage source of the interface branch in the fast system (V)

\( v_f \) Voltage at the interface in the fast subsystem (V)

\( V_{f, Exp} \) Expected voltage at the interface

\( V_{fint} \) Equivalent internal voltage sources in the fast subsystem

\( v_{f-ITI} \) Voltages at the interface using the ITI interface

\( v_{f-TLI} \) Voltages at the interface using the TLI interface

\( V_s \) Voltage source of the source branch in the slow system (V)

\( v_s \) Voltage at the interface in the slow subsystem (V)

\( V_{sint} \) Equivalent internal voltage sources in the slow subsystem

\( X_f \) State variables of the fast subsystem

\( X_s \) State variables of the slow subsystem

\( Y_{fAA'} \) Output variables of the interface in the state-space modelling of the fast subsystem

\( Y_{sAA'} \) Output variables of the interface in the state-space modelling of the slow subsystem

\( Z_f \) Characteristic impedance of the transmission line (Ω)

\( Z_f \) Equivalent impedance of the interface branch in the fast system

\( Z_s \) Equivalent impedance of the interface branch in the slow system

\( \rho \) Spectral radius of a matrix

\( \Phi(m) \) State transfer matrix of the entire coupled system within a large time-step

\( \Phi'(m) \) State transfer matrix of the entire coupled system using the single rate

\( \Phi_f(i) \) State transfer matrix of the fast subsystem in \( i \)th small time-step within one large time-step

\( \Phi_{non-int}(m) \) State transfer matrix of the entire coupled system without interpolation within a large time-step

\( \Phi_s \) State transfer matrix of the slow subsystem within a large time-step

\( \Phi_{non-int}(m) \) State transfer matrix of the fast system without interpolation within a large time-step

\( ||\Phi(m)||_p \) An arbitrary norm of the state transfer matrix of the entire coupled system
1. INTRODUCTION

1.1. Motivation

In order to achieve the 2020 targets, the share of the renewable energy will rise greatly in different EU members [1]. A DC grid is an emerging transmission technology, which can provide flexible control to interconnect various renewable energies. A “super grid” to connect most of European countries shows the ambition in offshore wind power integration[2]. Simultaneous simulation of power electronics based DC systems and associated AC systems is a critical requirement for studies of DC grids, such as protection, stability and AC/DC interaction [3]. However, current tools for power system simulation perform less efficiently in this simultaneous simulation. Electromechanical simulation can hardly perform the transient in DC grids due to its large time-steps, although they are suitable for large-scale AC power systems[4]. Electromagnetics transient programs are able to simulate the detailed response in DC grids with a small time-step, but the simulation for AC/DC integrated systems with a small time-step will experience dramatically long time [5, 6].

Parallel computation in Electromagnetics transient simulation has been used for power system simulation, using the same time-step and data exchanged every step [7]. A large-scale system can be partitioned into several sub-systems, each of which can run independently in a separate Central Processing Unit (CPU). Therefore, the total calculation time is only a portion of the original one. However, due to the limitation of data communication, CPU numbers can not be increased unlimitedly to speed up the simulation. Normally parallel computation for power system simulation uses 8-12 cores in a server or 4-6 cores in a PC [8]. An entire power system is too large to be simulated using the current parallel simulation technology with a single time step.

Different simulation rates can be used in parallel computation, which is called multi-rate parallel algorithm. Components of power system have their dynamics with different time constants naturally. Components in DC grids with small time constants, such as AC/DC converters, DC/DC transformers and DC breakers, must use small time-steps to capture precise responses, while the conventional AC elements, which have slow dynamics, can use a large time-step in order to make computation fast.[9].

Multi-rate parallel algorithms essentially divide a system into several subsystems, which are simulated with suitable time-steps, in order to improve simulation speed. Not only integrated AC/DC systems, but also industrial applications in thermal/electrical co-simulation [10, 11], mechanical/electrical system co-simulation [12] can benefit from these algorithms.

1.2. Literature survey

DC grids, as an emerging technology, attract extensive studies. In [13], two multi-terminal DC modelling are studied in DC power flow using the Newton method. However, the operation modes of DC converters are too simple to present the practical situation. In [14], more general and detailed multi-terminal DC modelling in the sequential AC/DC power flow algorithm is proposed considering the power losses of the converters. The DC power flow method is simpler and faster than the Newton method. In [15, 16], a state estimation approach for linear problems is utilized in order to estimate the DC power flow. In [17], to remove the measure error, the sparse-based error detection technology is used to obtain the accurate DC power flow results. To manage DC network power, the distributed power dispatch method is proposed to replace the original centralized manner, which can greatly reduce the computation burden [18].

The transient study is another important aspect for large-scale power system containing DC grids. [19] proposed the MTDC modeling including DC lines, converters and DC controllers for transient study. In [20], the saturation of a generator is considered in the transient studies to investigate the bifurcation issues.

Electromagnetic simulation is still served as a major method for DC grid analysis [21]. The transient issues in DC grids, such as the DC controllers and IGBTs, belong to small time-scales, which can only be simulated using electromagnetic programs. Multi-rate parallel simulation is a suitable technology to analyze the DC grids and the interaction between a DC grid and a large-scale power system. The singular perturbation method is used to solve fast-slow systems [22]. However this method is not suitable for power...
electronics devices of which both fast and slow dynamics must be investigated, because this method uses asymptotic solution which neglects fast transients of the systems.

In order to investigate the fast transient of DC grids, there are three major algorithms to perform the parallel computation, which can be extended to multi-rate simulations, the Ideal Transform Interface (ITI) [23], the Transmission Line Interface (TLI) [7, 24] and the Thevenin equivalent interface [25, 26].

There are concerns over instability and errors of the existing parallel methodologies for multi-rate simulation [23], which could limit the application for simulation of DC grids.

1.3. Contribution

This paper is to provide the systematic evaluation of the multi-rate simulation methods and investigate the stability and accuracy. The major contributions are listed as below:

1. An improved multi-rate method based on the Thevenin equivalent circuit, named Modified Thevenin Equivalent Interface (MTI), is proposed to overcome the issues of synchronization in multi-rate simulation.

2. Three theorems have been proposed and proved to illustrate the stability characteristics of ITI, TLI and MTI.

3. The simulation errors using ITI, TLI and MTI have been analytically evaluated and the factors of these errors have been obtained.

4. The systematic analysis and comparisons of ITI, TLI and MTI have been performed in terms of stability and accuracy.

1.4. Structure of paper

The rest of this paper is organized as follows. In Section II, the multi-rate simulation method ITI and TLI is presented, while an improved multi-rate method based on the Thevenin equivalent circuit, named Modified Thevenin Equivalent Interface (MTI), is proposed to make it more suitable for DC grid simulation. Section III presents the analytical evaluation of the stability of three multi-rate simulation method. In Section IV, the analytical evaluation of the simulation errors of these multi-rate simulation methods is evaluated analytically. Section V presents a simulation study of different multi-rate simulation using the practical five-terminal MTDC as the test cases. Section V concludes the paper.

2. Multi-rate Simulation Methods

2.1. Ideal Transform Interface (ITI)

A simple circuit, as shown in Fig.1, is used for analyzing multi-rate simulation algorithms.

Fig. 1 Example System.

When the ideal transform interface is used [23]. The original circuit can be partitioned into two subsystems, fast and slow systems, as shown in Fig.2.
The interface current source, $I_f$, is equal to the injected current of Subsystem 2, $i_s$. The interface voltage source $V_s$ is equal to the voltage of the current source in Subsystem 1, $v_f$.

The subsystems are simulated with a small time-step, $h$, and a large time-step, $H=mh$, respectively. Data from a slow system to a fast system between different subsystems use the interpolation, and data from the fast system to the slow system is through a zero-order holder [27].

The formula for $I_f$ and $V_s$ will be achieved as:

$$
\begin{align}
V_s(T+H) &= v_f(T) \\
I_f(t) &= \frac{H+T-t}{H} i_s(T-H) + \frac{t-T}{H} I_s(T)
\end{align}
$$

where $T$ is the simulation time instant in the slow system, and $t$ is in the fast system, which is within the interval $[T, T+H]$.  

2.2. Transmission Line Interface (TLI)

The transmission line interface is a widely applied method of parallel computation in AC systems [23, 24]. When there is no real transmission line in some areas of a power system, an inductor is introduced as an equivalent transmission line to decouple the system.

The source branches, $V_f$, $V_s$ and $Z_1$, represent the equivalent circuits of TLI terminals. $Z_1$ is the characteristic impedance of the transmission line, which is $2L_1/H$ in this example.

Similarly, interpolation is used for data from the slow system to the fast system, and a zero order hold is used from the fast to slow system. The decoupling equations are obtained:

$$
\begin{align}
V_s(T+H) &= V_f(T) + 2i_s(T)Z_1 \\
V_f(t) &= \frac{H+T-t}{H} (V_s(T-H) + 2i_s(T-H)Z_1) + \frac{t-T}{H} (V_f(T) + 2i_s(T)Z_1)
\end{align}
$$

2.3. Modified Thevenin Equivalent Interface (MTI)

The ITI and the TLI have induced the latency to realize decoupling, which causes errors of node voltages and currents between the two sides of the interface.

The Thevenin equivalent circuit interface can alleviate these errors [10, 11], as shown in Fig.4.
Fig. 4 MTI interface method.

$V_f, V_s, Z_f,$ and $Z_s$ represent Thevenin equivalent circuits of the interface [25]. The Thevenin equivalent circuit method is commonly used in single rate simulation, where the interface equations are:

$$
\begin{align*}
V_f(t+h) &= g_f(X_f(t)) \\
V_s(t+h) &= g_s(X_s(t), V_{\text{sint}}(T))
\end{align*}
$$

where $X_f$ represents state variables in the fast subsystem, e.g. $v_c$ in this case; $X_s$ represents state variables in the slow subsystem, $i_{L1}$ and $i_{L2}$ in this case; $g_f$ and $g_s$ are the functions to solve the open circuit voltages in the subsystems.

However, due to different rates, the slow subsystem requires the $V_s(t+H)$, to calculate results for the next step, $t+H$, at the time of $t$. Obviously, $V_s(t+H) \neq V_s(t+h)$. There is the same challenge for calculating $V_f$.

Traditional Thevenin equivalent interfaces assume that the mean between $V_s(t)$ and $V_s(t+H)$ is approximately equal to $V_s(t+H)$ or $V_s(t+h)$. Then, $V_s(t+H)$ is substituted by the average value. But the existed issue on the accuracy and convergence should still be solved [28].

To surmount these obstacles in achieving the synchronization, an improvement proposed in this paper is to select proper history values to replace these state variables.

When the simulation goes to the time of $t$, the simulation in the subsystem with a small time step continues, however, for the large time step subsystem, the simulation will come back to the time of $t+h-H$ and achieve an integration of $H$ to get the result of $t+h$. In this case, $V_s(t+h)$ is only required. A proper history time, $t+h-H$, is selected to make the slow subsystem compliant to the Thevenin equivalent source, $V_s(t+h)$.

Synchronization is thus achieved using accurate calculation based on the history value to avoid prediction which could cause unexpected errors and instability.

Rewriting (3), the decoupling equation will be obtained:

$$
\begin{align*}
V_f(t+h) &= g_f(X_f(t)) \\
V_s(t+h) &= \frac{t+H-t-h}{H} g_s(X_s(T), V_{\text{sint}}(t)) + \frac{T+H-t-h}{H} g_s(X_s(T+H), V_{\text{sint}}(T+H))
\end{align*}
$$

The MTI algorithm can decouple a system at any electrical nodes without losing the accuracy and stability. Therefore, subsystems with converters, e.g. wind generators, FACTS and more complicated DC grids, can be separated and run with different time-steps.

3. Stability Analysis

As illustrated in Fig. 5, a network can be partitioned into two sub-networks, interconnected to each other, through an interface, $AA'$. Sub-networks with fast dynamics and slow dynamics are represented by the subscript, $f$ or $s$, respectively.

![Diagram of partition system for multi-rate simulation](image)

Fig. 5 Partition system for multi-rate simulation.

The state space of two sub-networks are given in (5) and (6):
\[ \begin{align*}
\dot{X}_f &= A_f X_f + B_{f int} B_{f int, f int} U_{f int} U_{f int, f int}^T, \\
Y_{f int} &= C_f X_f + D_{f int} D_{f int, f int} U_{f int} U_{f int, f int}^T \\
\dot{X}_s &= A_s X_s + B_{s int} B_{s int, s int} U_{s int} U_{s int, s int}^T, \\
Y_{s int} &= C_s X_s + D_{s int} D_{s int, s int} U_{s int} U_{s int, s int}^T.
\end{align*} \]

(5)

\[ \begin{align*}
\dot{X}_f &= (I-hA_f)^{-1} X_f (mk) + (I-hA_f)^{-1} h B_{f int} B_{f int, f int} U_{f int} U_{f int, f int} (mk+1) \\
Y_{f int} &= C_f X_f (mk+1) + D_{f int} D_{f int, f int} U_{f int} U_{f int, f int} (mk+1) \\
\dot{X}_s &= (I-mhA_s)^{-1} X_s (mk) + (I-mhA_s)^{-1} mh B_{s int} B_{s int, s int} U_{s int} U_{s int, s int} (mk+m) \\
Y_{s int} &= C_s X_s (mk+m) + D_{s int} D_{s int, s int} U_{s int} U_{s int, s int} (mk+m). \\
\end{align*} \]

(6)

where \( U_{f int} = V_{f int} \) and \( U_{s int} = V_{s int} \) are the input variables of each sub-network. \( U_{f int} \) and \( U_{s int} \) are the input variables from the interface. \( Y_{f int} \) and \( Y_{s int} \) represent output variables of subsystems.

By using the Backward Euler, the discrete type of equation (5) and (6) are obtained in (7) and (8),

\[ \begin{align*}
X_f (mk+i) &= (I-hA_f)^{-1} X_f (mk+i-1) + (I-hA_f)^{-1} h B_{f int} B_{f int, f int} U_{f int} U_{f int, f int} (mk+i) \\
Y_{f int} (mk+i) &= C_f X_f (mk+i) + D_{f int} D_{f int, f int} U_{f int} U_{f int, f int} (mk+i) \\
X_s (mk+m+i) &= (I-mhA_s)^{-1} X_s (mk+m+i-1) + (I-mhA_s)^{-1} mh B_{s int} B_{s int, s int} U_{s int} U_{s int, s int} (mk+m+i) \\
Y_{s int} (mk+m+i) &= C_s X_s (mk+m+i) + D_{s int} D_{s int, s int} U_{s int} U_{s int, s int} (mk+m+i) \\
\end{align*} \]

(7)

\[ \begin{align*}
X_f (mk+m) &= (I-hA_f)^{-1} X_f (mk) + (I-hA_f)^{-1} h B_{f int} B_{f int, f int} U_{f int} U_{f int, f int} (mk+m) \\
Y_{f int} (mk+m) &= C_f X_f (mk+m) + D_{f int} D_{f int, f int} U_{f int} U_{f int, f int} (mk+m) \\
X_s (mk+m) &= (I-mhA_s)^{-1} X_s (mk+m) + (I-mhA_s)^{-1} mh B_{s int} B_{s int, s int} U_{s int} U_{s int, s int} (mk+m) \\
Y_{s int} (mk+m) &= C_s X_s (mk+m) + D_{s int} D_{s int, s int} U_{s int} U_{s int, s int} (mk+m). \\
\end{align*} \]

(8)

in which, \( k \) represents steps in the simulation.

For the stability analysis, the input variables inside each sub-network can be set as 0, which means \( U_{f int} \) and \( U_{s int} \) are 0.

The stability of integration algorithms indicates whether or not errors will be accumulated during the integration [29], no matter if the system under study is stable or not. Stability is an important index of simulation accuracy, which can be determined through the system norms [30].

3.1. Stability Analysis for ITI

3.1.1 State Transfer Matrix for ITI

Assuming that the input variables in the fast subsystem, \( U_{f int} \), are the injected current through the interface, \( I_{f int} \), and the input variables in the slow subsystem, \( U_{s int} \), is the voltages on the interface, \( V_{s int} \). The output variables, \( Y_{f int} \) and \( Y_{s int} \), are the \( I_{f int} \) and \( V_{s int} \), respectively. To be simplified, \( V_{s int} \) is denoted as \( V \) and \( I_{f int} \) is denoted as \( I \).

According to Fig.5, rewrite the decoupling equation (1) in the forms of discretion,

\[ \begin{align*}
V_i (mk) &= V_{f int} (mk-m) \\
I_i (mk+i) &= \frac{m-i}{m} I_{s int} (mk-m) + \frac{i}{m} I_{s int} (mk). \\
\end{align*} \]

(9)

where \( i \) represent the \( i \)th steps between \( mk \) and \( m(k+1) \).

Considering the decoupling equation (9), the discrete equation can be rewritten:

\[ \begin{align*}
X_f (mk+i) &= (I-hA_f)^{-1} X_f (mk+i-1) + (I-hA_f)^{-1} h B_{f int} B_{f int, f int} U_{f int} U_{f int, f int} (mk+i) \\
V_i (mk+i) &= C_f X_f (mk+i) + D_{f int} D_{f int, f int} U_{f int} U_{f int, f int} (mk+i) \\
X_s (mk+m+i) &= (I-mhA_s)^{-1} X_s (mk+m+i-1) + (I-mhA_s)^{-1} mh B_{s int} B_{s int, s int} U_{s int} U_{s int, s int} (mk+m+i) \\
V_i (mk+m+i) &= C_s X_s (mk+m+i) + D_{s int} D_{s int, s int} U_{s int} U_{s int, s int} (mk+m+i) \\
X_f (mk+m) &= (I-hA_f)^{-1} X_f (mk) + (I-hA_f)^{-1} h B_{f int} B_{f int, f int} U_{f int} U_{f int, f int} (mk+m) \\
V_i (mk+m) &= C_f X_f (mk+m) + D_{f int} D_{f int, f int} U_{f int} U_{f int, f int} (mk+m) \\
X_s (mk+m) &= (I-mhA_s)^{-1} X_s (mk+m) + (I-mhA_s)^{-1} mh B_{s int} B_{s int, s int} U_{s int} U_{s int, s int} (mk+m) \\
V_i (mk+m) &= C_s X_s (mk+m) + D_{s int} D_{s int, s int} U_{s int} U_{s int, s int} (mk+m). \\
\end{align*} \]

(10)

\[ \begin{align*}
X_f (mk+m) &= (I-hA_f)^{-1} X_f (mk) + (I-hA_f)^{-1} h B_{f int} B_{f int, f int} U_{f int} U_{f int, f int} (mk+m) \\
V_i (mk+m) &= C_f X_f (mk+m) + D_{f int} D_{f int, f int} U_{f int} U_{f int, f int} (mk+m) \\
X_s (mk+m) &= (I-mhA_s)^{-1} X_s (mk+m) + (I-mhA_s)^{-1} mh B_{s int} B_{s int, s int} U_{s int} U_{s int, s int} (mk+m) \\
V_i (mk+m) &= C_s X_s (mk+m) + D_{s int} D_{s int, s int} U_{s int} U_{s int, s int} (mk+m). \\
\end{align*} \]

(11)

There are \( m \) steps of simulation with a small time-step for the fast system, whose state transfer equation for the \( m \)th calculation is shown in (12), and one step of simulation with a large time-step for the slow system, whose state transfer equation is shown in (14).
\[
[X_f(k+1) X_s(k) I_f(k) V_s(k)] = \prod_{i=1}^{2} \Phi_f(i) [X_f(k) X_s(k) I_f(k) V_s(k)]
\]  

where the \(\Phi_f(i)\) represents the transfer matrix in the \(i\)th small step within one large time-step, as below:

\[
\Phi_f(i) = \begin{bmatrix}
A_{11} & A_{12} & A_{13} & A_{14} \\
0 & 0 & 0 & 0 \\
\end{bmatrix}
\]

(13)

where \(A_{11} = (I-hA_f)^{-1} A_1 = (I-hA_f)^{-1} hB_{sffe}/mC_s\), \(A_{12} = (I-hA_f)^{-1} hB_{sffe}(m-i)/m\) and \(A_{13} = (I-hA_f)^{-1} hB_{sffe}/mD_{sffe}\).

\[
[X_f(k) X_s(k+1) I_f(k+1) V_s(k+1)] = \Phi_s [X_f(k) X_s(k) I_f(k) V_s(k)]
\]  

(14)

where \(\Phi_s\) represents the transfer matrix of the slow system as below:

\[
\Phi_s = \begin{bmatrix}
I & 0 & 0 & 0 \\
A_{1,1} & A_{1,2} & A_{1,3} & A_{1,4} \\
0 & C_s & 0 & D_{sffe} \\
C_f & 0 & D_{sffe} & 0 \\
\end{bmatrix}
\]

(15)

where \(A_{1,1} = (I-mhA_s)^{-1} mhB_{sffe}C_s\), \(A_{1,2} = (I-mhA_s)^{-1} A_{1,3} = (I-mhA_s)^{-1} mhB_{sffe}D_{sffe}\).

Combine (12) and (14), the transfer state equations of whole system can be obtained:

\[
[X_f(k+1) X_s(k+1) I_f(k+1) V_s(k+1)] = \Phi(m) [X_f(k) X_s(k) I_f(k) V_s(k)]
\]  

(16)

where \(\Phi(m)\) represents the state transfer as below:

\[
\Phi(m) = \text{diag}[0, I_{n_1}] \Phi_f + \text{diag}[I_{n_2}, 0] \prod_{i=1}^{2} \Phi_f(i)
\]

(17)

where \(\text{diag}[0, I_{n_1}]\) represents a diagonal matrix, \(n_1\) is the summary of the dimension of \(X_s\), \(I_f\) and \(V_s\), and \(n_2\) is the dimension of the dimension of \(X_f\).

This matrix, \(\Phi(m)\), fully represents the dynamics of this multi-rate simulation and is thus used to evaluate the algorithm stability.

### 3.1.2 Stability Theorem for Multi-rate without Interpolation

The norm of state transfer matrix determines the algorithm stability. The criteria [31] is that a system is stable, when

\[
\rho(\Phi(m)) \leq ||\Phi(m)||_p \leq 1
\]

(18)

where \(||\Phi(m)||_p\) represents an arbitrary norm, \(\rho\) represents the spectral radius of a matrix.

**Theorem 1:**

If simulation of ITI using a single rate is stable, and the divided fast and slow subsystems are also asymptotically stable, then the multi-rate simulation algorithm without interpolation is stable as long as all the multiple rates are not larger than the single rate.

**Proof:**

Discretizing (5) and (6) in the large time-step, \(H = mh\), and combining the decoupling equation (9) when \(i = m = 1\), a new state transfer function can be obtained:

\[
[X_f(k+1) X_s(k) I_f(k) V_s(k)] = \Phi(m) [X_f(k) X_s(k) I_f(k) V_s(k)]
\]

(19)

Then, the following state transfer function can be derived:

\[
[X_f(k+1) X_s(k+1) I_f(k+1) V_s(k+1)] = \Phi(m) [X_f(k) X_s(k) I_f(k) V_s(k)]
\]

where \(\Phi'(m)\) represents the state transfer, whose norm should be less than 1, as below:

\[
\Phi'(m) = \begin{bmatrix}
A_{1,1}' & A_{1,2}' & A_{1,3}' & A_{1,4}' \\
A_{2,1} & A_{2,2} & A_{2,3} & A_{2,4} \\
0 & C_s & 0 & D_{sffe} \\
C_f & 0 & D_{sffe} & 0 \\
\end{bmatrix}
\]

(20)

\[
||\Phi'(m)|| < 1
\]

(21)
where $A_{21}, A_{22}, A_{23}$ is the same as (15) and $A_{11}, A_{12}, A_{13}, A_{14}$ is as below.

$$A_{11}=(I-HA_{f})^{-1}A_{12}=(I-FA_{f})^{-1}HB_{fse}C_{s}, A_{13}=0, A_{14}=(I-FA_{f})^{-1}HB_{fse}D_{sle}$$

Find a matrix $\Phi'(m)$ to make $\Phi'(m)=\Phi'(m)\Phi$, then

$$\Phi'(m)=\begin{bmatrix} A_{11} & A_{12} & A_{13} & A_{14} \\ A_{21} & A_{22} & A_{23} & 0 \\ 0 & C_{f} & 0 & D_{sle} \\ C_{f} & 0 & D_{sle} & 0 \end{bmatrix}$$

(22)

where $A_{s1}=(I-FA_{f})^{-1}$.  

When the data exchange for the multi-rate simulation without interpolating and smoothing process, rewrite the decoupling equation (9) in the forms of discretion.

$$\begin{cases} V'(mk)=V_{fse}(mk-m) \\ I_{f}(mk+i)=I_{sle}(mk) \end{cases}$$

(23)

Using the similar derivation from (10) to (16), the state transfer, $\Phi_{\text{non-int}}(m)$, matrix can be obtained:

$$\Phi_{\text{non-int}}(m)=\begin{bmatrix} A_{11} & A_{12} & A_{13} & A_{14} \\ A_{21} & A_{22} & A_{23} & 0 \\ 0 & C_{f} & 0 & D_{sle} \\ C_{f} & 0 & D_{sle} & 0 \end{bmatrix}$$

(24)

where $A_{21}, A_{22}, A_{23}$ is the same as (15) and $A_{11}, A_{12}, A_{13}, A_{14}$ is as below,

$$A_{11}=(I-FA_{f})^{-m}A_{12}=\sum_{i=0}^{m}((I-FA_{f})^{-1}hB_{fse}C_{s})^{i}$$

$$A_{13}=0, A_{14}=(I-FA_{f})^{-1}HB_{fse}D_{sle}$$

Find a matrix $\Phi_{\text{non-int}}(m)$ to make $\Phi_{\text{non-int}}(m)=\Phi_{\text{non-int}}(m)\Phi$, then $\Phi_{\text{non-int}}(m)$ is as below:

$$\Phi_{\text{non-int}}(m)=\begin{bmatrix} A_{11} & A_{12} & A_{13} & A_{14} \\ A_{21} & A_{22} & A_{23} & 0 \\ 0 & C_{f} & 0 & D_{sle} \\ C_{f} & 0 & D_{sle} & 0 \end{bmatrix}$$

(25)

where $A_{s1}=(I-FA_{f})^{-m}$

Comparing with the norm of $A_{s1}$ and $A_{s1}'$, when the norm of $A_{f}$ is less than 1, which is the requirement of the stability in the fast subsystem, the norm of $A_{s1}$ is less than the norm of $A_{s1}'$. Also, the norm of $A_{s1}$ and $A_{s1}'$ will determine the norm of $\Phi_{\text{non-int}}(m)$ and $\Phi'(m)$, respectively.

Then, an inequality can be obtained:

$$||\Phi_{\text{non-int}}(m)||<||\Phi'(m)||$$

(26)

Because $\Phi'(m)=\Phi'(m)\Phi$ and $\Phi_{\text{non-int}}(m)=\Phi_{\text{non-int}}(m)\Phi$, then the norm of $\Phi_{\text{non-int}}(m)$ can be achieved as:

$$||\Phi_{\text{non-int}}(m)||<||\Phi'(m)||<1$$

(27)

This proves the multi-rate integration method without interpolation is stable and the Theorem 1 is proved.

3.1.3 Stability Analysis for Multi-rate with Interpolation

The transfer matrix $\Phi(m)$ in (16) can be transferred to $\Phi(m)=\Phi'(m)\Phi$, then $\Phi'(m)$ is given as below:

$$\Phi'(m)=\begin{bmatrix} A_{11} & A_{12} & A_{13} & A_{14} \\ A_{21} & A_{22} & A_{23} & 0 \\ 0 & C_{f} & 0 & D_{sle} \\ C_{f} & 0 & D_{sle} & 0 \end{bmatrix}$$

(28)

where $A_{s1}=(I-FA_{f})^{-m}C_{f}\sum_{i=0}^{m}((I-FA_{f})^{-1}hB_{fse}D_{sle})^{m-i}D_{sle}^{-1}$

Similarly to the previous section, if the norms of $A_{s1}$ are less than the norms of $A_{s1}'$, the norms of $\Phi(m)$ can be found less than $\Phi'(m)$ and less than 1, which means that the multi-rate integration method with interpolation is stable.

However, the norms of $A_{s1}$ can not be directly proved to be less than $A_{s1}'$, some tools can help to calculate the norm of $A_{s1}$ to evaluate the stability of the multi-rate simulation with interpolation in the cases. Normally, it is stable as well.
In conclusion, with a large time-step and a small time-step, the stability of the multi-rate algorithm of ITI is better than the parallel computation of ITI with the single rate of the same large time-step. However, [23] indicates that the parallel computation of ITI with a single rate hardly keeps the stability, because of the artificial induced latency.

3.2. Stability Analysis for TLI

3.2.1. State Transfer Matrix for TLI

As illustrated in Fig.3, \( V_f \) and \( V_s \) of the equivalent source branches of TLI are the input variables, \( U_f \) and \( U_s \), in fast and slow subsystems. The output variables \( Y_f \) and \( Y_s \) represent the \( V(mk-m)+2i(mk-m)Z_i \), denoted as \( Y_f \) and \( Y_s \) for simplification.

Rewrite the equation (2) in the forms of discretion,

\[
\begin{align*}
V_f(mk) &= m^{-i}Y_f(mk-m) + \frac{i}{m} Y_s(mk) \\
V_s(mk) &= Y_f(mk-m)
\end{align*}
\] (29)

Considering the decoupling equation (29), using the system equation (7) and (8), the discrete equations can be achieved. In fact, they are as same as (10), (11), except that \( V_f \) replaces \( I_f \).

Similarly, deriving from (12) to (16), the state transfer matrix \( \Phi(m) \) can be obtained, as below, to evaluate the stability of the TLI.

\[
\Phi_{\text{TLI}}(m) = \begin{bmatrix}
A_{11} & A_{12} & A_{13} & A_{14} \\
A_{21} & A_{22} & A_{23} & 0 \\
0 & C & 0 & D_{\text{f,s}} \\
C_f & 0 & D_{\text{f,s}} & 0
\end{bmatrix}
\] (30)

where \( A_{21}, A_{22}, A_{23} \) is the same as (15) and \( A_{11}, A_{12}, A_{13}, A_{14} \) is,

\[
\begin{align*}
A_{11} &= (1-hA_f)^{-m} \\
A_{21} &= \sum_{i=1}^{m}(1-hA_f)^{-i} m^{-i} h^{-i} B_{\text{f,s}} C \\
A_{13} &= \sum_{i=1}^{m}(1-hA_f)^{-i} m^{-i} h^{-i} B_{\text{f,s}} A_{14} = \sum_{i=1}^{m}(1-hA_f)^{-i} m^{-i} h^{-i} B_{\text{f,s}} D_{\text{f,s}}
\end{align*}
\]

3.2.2. Stability Analysis for Multi-rate Algorithm

**Theorem 2:**

If the simulation of TLI with a single rate is stable, and the divided fast and slow subsystems are also asymptotically stable, then the multi-rate simulation algorithm without interpolation is stable as long as all the multiple rates are not larger than the single rate.

The assumption of this theorem includes that:

- A system is divided into subsystems;
- Stable simulation can be achieved for the subsystems connected through TLI interface using the same time-steps for all subsystems, called a single rate, \( H \). This means that the state transfer matrix in an equation, similar to (21), has the norms less than 1;
- Each subsystem is asymptotically stable, which means that the \( A_{11} \) and \( A_{22} \) in (30) have norms less than 1;
- Different simulation time-steps, which are not larger than the single rate \( H \), are used for subsystems. Fast subsystems have small time-steps and slow subsystems have relatively large time-steps.

When the above assumptions are met, stable simulation can be achieved for the fast and slow subsystems connected through TLI.

This theorem can be proved using the similar procedure from (19) to (27).

For a similar matrix \( A_{11} \) in (31) can be achieved for the multi-rate TLI with interpolation in order to compare with \( A_{11} \) in (30).

\[
A_{11} = ((1-hA_f)^{-1})^m - C_f \sum_{i=1}^{m}(1-hA_f)^{-i} m^{-i} h B_{\text{f,s}} m^{-i} D_{\text{f,s}}^{-1}
\] (31)

Rewrite (31):
\[ A_{s11} = (I - hA_f)^{-1} \left( \frac{(1 - C_f)(1 - hA_f)^{-1} hB_{fdis} \sum_{i=1}^{m} ((1 - hA_f)^{-1})^{m-i} D_{fdis}^{-1}}{m} \right) \]  

(32)

Normally, the stability of multi-rate algorithm is usually met by calculating the norm of \( A_{s11} \).

In conclusion, for TLI, the stability of the simulation with different rates depends on the stability of the simulation with the single rate of a large time-step. However, [23] indicates that simulation of the TLI with a single rate always keeps stability.

3.3. Stability Analysis for MTI

3.3.1 State Transfer Matrix for MTI

\( U_{fAA'} \) and \( U_{sAA'} \) represent the interface voltages at the two sides respectively. \( Y_{fAA'} \) and \( Y_{sAA'} \) represent the currents through the interface.

Rewrite the relationship of voltages and currents in the interface in the forms of discretion.

\[
\begin{align*}
U_{fAA'}(mk) &= U_{sAA'}(mk) \\
Y_{fAA'}(mk) &= -Y_{sAA'}(mk)
\end{align*}
\]  

(33)

Using the system equations (7), (8) and (33) and removing the intermediate variables, \( U_{fAA'} \), \( U_{sAA'} \) and \( Y_{fAA'} \) and \( Y_{sAA'} \), the discrete equations can be rewritten:

\[
\begin{align*}
X_f'(mk+i) &= A_f X_f'(mk+i-1) + A_{sf}(m-i) X_f'(mk-m) + i X_f'(mk) \\
X_f'(mk+m) &= A_f X_f'(mk+m-1) + A_{sf} X_f'(mk)
\end{align*}
\]  

(34)

where \( A_f = A_{fdis}B_{fdis}A_{fdis} \), \( A_{sf} = -B_{fdis}I A_{fdis} \), \( A_{sdis} = (I-hA_f)^{-1} A_{sdis}B_{sdis}A_{sdis} \), \( A_{sf} = -B_{sdis}I A_{sdis} \), \( A_{sdis} = (I-hA_f)^{-1} A_{sdis}B_{sdis}A_{sdis} \).

Using the system equations (7), (8) and (33) and removing the intermediate variables, \( U_{fAA'} \), \( U_{sAA'} \) and \( Y_{fAA'} \) and \( Y_{sAA'} \), the discrete equations can be rewritten:

\[
\begin{align*}
X_f'(mk+i) &= A_f X_f'(mk+i-1) + A_{sf}(m-i) X_f'(mk-m) + i X_f'(mk) \\
X_f'(mk+m) &= A_f X_f'(mk+m-1) + A_{sf} X_f'(mk)
\end{align*}
\]  

(34)

where \( \Phi_f(i) \) represents the transfer matrix in the \( i \)th step among a large time-step, as below:

\[
\begin{bmatrix}
X_f'(mk+i) \\
X_f'(mk) \\
X_f'(mk-m)
\end{bmatrix} = \Phi_f(i) \begin{bmatrix}
X_f'(mk+i-1) \\
X_f'(mk) \\
X_f'(mk-m)
\end{bmatrix}
\]  

(35)

where \( \Phi_f(i) \) represents the transfer matrix of the slow system:

\[
\Phi_f(i) = \begin{bmatrix}
A_f & A_{sf} & m-i \\
0 & m & 1
\end{bmatrix}
\]  

(36)

(37)

where \( \Phi_s \) represents the transfer matrix of the slow system:

\[
\Phi_s = \begin{bmatrix}
A_f & A_{sf} & 0 \\
A_f & A_{sf} & 0 \\
0 & I & 0
\end{bmatrix}
\]  

(38)

The whole system can be derived for its state transfer matrix, represented by \( \Phi(m) \),

\[
\Phi(m) = \Phi_f \prod_{i=1}^{m} \Phi_f(i)
\]  

(39)

3.3.2 Stability Analysis

**Theorem 3:**

If the original system is asymptotically stable, and the divided fast and slow subsystems are also asymptotically stable. Then simulation using proposed MTI algorithm is stable.
Proof:
Let $A_{ff}$ and $A_{ss}$ be the discrete state matrix of the fast system and the slow system respectively, the norm of $A_{ff}$ and $A_{ss}$ is less than 1, therefore according to (36), the norm of $\Phi_f(i)$ is less than 1.

Meanwhile, the state matrix of the original system can be obtained through eliminating the variable $U_{sAA'}$ and $Y_{sAA'}$, represented by $A$.

$$
\begin{bmatrix}
\dot{X}_f \\
\dot{X}_s
\end{bmatrix} =
\begin{bmatrix}
A_{ff} - B_{fxt}(D_f + D_s)^{-1} C_f & -B_{fxt}(D_f + D_s)^{-1} C_s \\
-B_{fxt}(D_f + D_s)^{-1} C_f & A_{ss} - B_{sxt}(D_f + D_s)^{-1} C_s
\end{bmatrix}
\begin{bmatrix}
X_f \\
X_s
\end{bmatrix}
$$

Also, suppose $A(m)$, which can be factorized as below.

$$
A(m) =
\begin{bmatrix}
I & 0 \\
0 & ml
\end{bmatrix}
\begin{bmatrix}
A_{ff} - B_{fxt}(D_f + D_s)^{-1} C_f & -B_{fxt}(D_f + D_s)^{-1} C_s \\
-mB_{sxt}(D_f + D_s)^{-1} C_f & mA_{ss} - mB_{sxt}(D_f + D_s)^{-1} C_s
\end{bmatrix}
$$

(40)

(41)

$$
A(m) \text{ is used as the state matrix of a new system. Since the original system absolute stable, all eigenvalues of the } A \text{ has the negative real part. Then, all eigenvalues of the } A(m) \text{ has the negative real part. It deduces that the new system is absolutely stable.}

\begin{equation}
X_f(k+1) = (A_{f-dis} - B_{fxt-dis} \Delta C_f A_{f-dis}) X_f(k)
- B_{fxt-dis} \Delta C_s A_{s-dis} X_s(k)
\end{equation}

\begin{equation}
X_s(k+1) = -B_{sxt-dis} \Delta C_f A_{f-dis} X_f(k)
+ (A_{s-dis} - B_{sxt-dis} \Delta C_s A_{s-dis}) X_s(k)
\end{equation}

(42)

And \( \Delta = (C_s B_{sxt-dis} + D_{sxt} + D_{xt-dis} + C_f B_{fxt-dis})^{-1} \)

The state transfer matrix is $\Phi_{sll}$, whose norms is less than 1.

$$
\Phi_{sll} =
\begin{bmatrix}
A_{f} & A_{0} \\
A_{f} & A_{ss}
\end{bmatrix}
$$

(43)

This matrix is found to be just the sub-matrix of $\Phi_s$ in (38). Thus the norms of $\Phi_s$ is less than 1.

Follow that,

$$
\Phi(m) = \Phi_s^{m} \prod_{i=1}^{m} \Phi_f(i) < 1
$$

(44)

Hence, the MTI algorithm is stable as long as the original system is stable.

3.4. Stability Evaluation

In this part, the stability of the example system in Fig.1 will be studied to verify the proposed theorems of ITI, TLI and MTI.

The norm of the systems in both the multi-rate simulation and the single-rate simulation using large time-steps were calculated using the eigenvalue method for various circuit parameters, as shown in Fig.6-8. The comparison involves three interfaces. The dash line is the boundary of stability, as the legend of "ref".

Considering ITI, the norms of the system in the single-rate simulation are always larger than the multi-rate simulation. This verifies the Theorem 1. Also, the multi-rate method of ITI is stable, as long as ITI parallel simulation with a single-rate is stable. However, although the original system is stable, ITI parallel simulation with a single-rate becomes unstable when the norms exceed the "ref" value, as shown in Fig.6(a) and 7(a). This is due to artificial time delay between subsystems [14].

Considering TLI, the norms of the system in the single-rate simulation are always larger than in the multi-rate simulation. This verifies the Theorem 2. In most cases, the TLI is stable. However, the transmission line can also reduce the stability of simulation, compared with lumped inductors.

Among three multi-rate method, the stability of MTI is always the most stable under all conditions. Because no simplification is required in the MTI, the stability of this method is just slightly lower than the original simulation, and higher than the previous two methods. This verifies the Theorem 3.
4. Error Analysis

This section evaluates factors influencing errors of the multi-rate simulation. These include time-steps, interface inductor and parameters in both small and large time-step networks.

4.1. Decoupling Interface errors

For ITI, the equivalent circuit is shown in Fig.9 (a).

\[
\begin{align*}
V_{eqf}(s) & = G_f(s) V_f(s) \\
V_{eqs}(s) & = G_s(s) V_s(s)
\end{align*}
\]

where \( V_{eqf}(s) \) and \( V_{eqs}(s) \) represent the Thevenin equivalent sources of the subsystems, and \( G_f(s) \) and \( G_s(s) \) are the impedance in the equivalent sources.
Because there are the induced latency in ITI as below,

\[
\begin{cases}
I_i (s)=i_i (s)e^{-iH} \\
V_j (s)=v_j (s)e^{-iH}
\end{cases}
\]

The voltage, \(v_s\) in the interface can be derived as below,

\[
v_f^{-\text{ITI}} = \frac{v_{eq}(s)e^{-iH}G_f (s)}{G_i (s)+e^{-iH}G_f (s)} + \frac{v_{eq}(s)e^{-2iH}G_f (s)}{G_i (s)e^{-2iH}+G_f (s)}
\]

Alternatively, the expected result is as below,

\[
v_{f^{-\text{Exp}}} = \frac{v_{eq}(s)G_f (s)}{G_i (s)+G_f (s)} + \frac{v_{eq}(s)G_f (s)}{G_i (s)+G_f (s)+G_f (s)}
\]

The time-step \(H\) is always small, so that the \(e^{-2iH}\) is close to 1, it can be assumed that

\[G_f (s)+e^{-2iH}G_f (s)=G_f (s)+G_f (s)=e^{-2iH}G_f (s)+G_f (s)\]

Therefore, the error can be obtained as below,

\[e_v = \frac{v_f^{-\text{ITI}}-v_{f^{-\text{Exp}}}}{v_{f^{-\text{Exp}}}} = |e^{-iH}-1|
\]

For TLI, an inductor is used as an equivalent transmission line. The difference between the original circuit and the circuit with a transmission line is shown in Fig.9 (b), where the associated capacitor of the equivalent transmission line is \(C_{\text{TLI}}\), which is the can be obtained , \(C_{\text{TLI}}=H^2/L_1\).

The voltage in the interface for the original system and the circuit with TLI interface can be obtained:

\[
v_f^{-\text{TLI}} = \frac{v_{eq}(s)(L_1s+G_f (s))+v_{eq}(s)(G_f (s)+L_1s+L_1C_{\text{TLI}}s^2G_f (s))}{G_i (s)+L_1s+L_1C_{\text{TLI}}s^2G_f (s)+G_f (s)+G_f (s)+G_f (s)sC_{\text{TLI}}}
\]

\[
v_{f^{-\text{Exp}}} = \frac{v_{eq}(s)(G_f (s)+L_1s)}{G_i (s)+G_f (s)+L_1s} + \frac{v_{eq}(s)(G_f (s)+L_1s)}{G_i (s)+G_f (s)+G_f (s)+L_1s}
\]

Because the time-step \(H\) is very small, and the \(L_1C_{\text{TLI}} = H^2\) are close to 0,

\[G_f (s)+L_1s+L_1C_{\text{TLI}}s^2G_f (s)=G_f (s)+L_1s
\]

\[G_f (s)+L_1s+L_1C_{\text{TLI}}s^2G_f (s)+G_f (s)+G_f (s)sC_{\text{TLI}}
\]

\[=G_f (s)+L_1s+G_f (s)+G_f (s){G_f (s)sC_{\text{TLI}}}
\]

Therefore, the error can be obtained as below,

\[e_v = \frac{v_f^{-\text{TLI}}-v_{f^{-\text{Exp}}}}{v_{f^{-\text{Exp}}}} = |G_f (s)+G_f (s)sC_{\text{TLI}}|
\]

The error is dependent on the associated capacitor, and the configuration of the internal circuits affects the error as well. Stability analysis indicates that the errors of the ITI and TLI can only be evaluated when the simulation is stable.

For the MTI, the external circuit is exactly represented using Thevenin equivalent circuit, and no artificial simplification is involved in the method. Therefore, errors of MTI are much smaller than the previous too.

Also, Errors evaluation can be obtained as below [32]:

\[e(kH)<||R(m)||KHe^{FKH}-||R(m)||KH\]

where \(||R(m)||\) is a constant for a given \(m\). \(K\) and \(F\) are constants, which can be calculated with the system parameters. \(k\) represents the \(k\)th steps in the simulation.

This indicates that the maximum simulation errors within a simulation stage are limited and can be reduced as long as \(H\), the large time-step, is small enough. And also, along with the time constants of the subsystems increase, \(K\) and \(F\) may increase to enlarge the errors, but the detailed evaluation of \(K\) and \(F\) is still required.
4.2. Error Evaluation

The accuracy of the example system in Fig.1 will be studied to verify the theoretical analysis of errors of the ITI, TLI and MTI. The error evaluation for various conditions is illustrated in Fig.10 to 15. The dark lines represent simulation errors, while the grey line represents expected errors in theory for ITI and TLI, and as expected errors of MTI are exceedingly small, which are not shown in figures.

The results of increasing the large time-step are shown in Fig.10. The errors of ITI in Fig.10(a) increase due to the increased delay as proved in (49) and are approximately proportional to the increase in the large time-step. The trends of expected errors (ITI_Exp) and simulation errors (ITI) are the same, although there is about 20% difference between them. The errors of TLI in Fig.10(b) increase due to the increased associated capacitor, \( C_{TLI} \), as proved in (53). They are large than ITI, and the expected errors (TLI_Exp) are coincide with the simulation ones (TLI). The errors of MTI in Fig.10(c) are much less than others, only being lower than 0.03%.

Increasing of a small time-step doesn’t affect the errors, as shown in Fig.11, as the artificial delay of ITI and the associated capacitor of TLI are not increased. The simulation errors of ITI and TLI are close to their expected errors. Errors of MTI have no increment.

As shown in Fig.12, higher errors could be found for signals with higher frequency. \( s \) in formulas of errors, (49) and (53), is determined by the frequency. A little difference between the simulation errors and the expected error exists. Simulation errors of TLI increase with the same rate of expected errors. The errors of MTI increases, still being smaller than 0.01%.

Increasing in the interface inductance \( L_1 \) in Fig.1 can increase the associated capacitor of TLI to increase the errors, as shown in Fig.13, but it doesn’t affect the errors of ITI. Besides, The errors of MTI increase, reaching around 0.015%.

![Fig. 10 Error evaluation using various large time-steps.](image1)

![Fig. 11 Error evaluation using various small time-steps.](image2)
Increasing of the inductor $L_2$ in Fig. 1 can increase the time constant in the large time-step subsystem, while the capacitor $C_1$ can increase the time constant in the small time-step subsystem. The results, as shown in Fig. 14 and Fig. 15, show the expected errors of ITI and TLI are close to the simulation errors, which means the errors of evaluation are sensible.

4.3. Unstable Case Study

The accuracy consists of two parts: error range induced by modelling simplification at each step and error accumulation throughout simulation which is defined as the stability of a simulation algorithm. Errors using the MTI are much smaller than using the ITI and TLI, because the MTI does not need simplification. In addition, the MTI is more stable than the ITI and TLI, so that the rate of error accumulation of MTI is slower than the other two methods for an unstable case. Therefore, the simulation accuracy of the MTI is always higher than the ITI and TLI.
Although the stability of the MTI has not been proved for unstable systems, simulation using the MTI for unstable systems is acceptable, because the MTI achieves smaller errors than the ITI and TLI which have already been widely used for practical applications.

In order to study the performance of the MTI for unstable systems, the example system has been modified to become unstable. The detailed parameters are listed in Appendix C.

![Graph](image)

In Fig. 16, the original system is unstable, and the interface current, \(i_f\), is increasing with oscillation. The MTI curve and the accurate result (Acc) are in agreement in Fig.16(a). The ITI and TLI can generate larger errors. The error rate (the difference between the accurate results and multi-rate results divided by RMS of the accurate results) is illustrated in Fig.16(b). The error rates of the ITI and TLI increases as the simulation proceeds, while the error of the MTI almost doesn’t increase. The results with the MTI follow the accurate results well, although the system itself is unstable.

5. CASE STUDY

Although a linear system was used in theoretical analyses of multi-rate algorithms, the proposed multi-rate algorithm can be used for practical nonlinear systems with acceptable small errors, which are verified in this section.

This paper makes use of the multi-rate simulations to study the Zhou Shan Multi-terminal HVDC (MTDC) [33], as show in Fig.17.

The local AC power system, as shown in, includes 28 buses and 1 generator, the topology of DC system consists of 5 AC/DC converter stations connect AC system to DC system. Modular Multi-level Converters (MMC) are replaced by two-level converters, in order to avoid the influence of the complex modelling of MMC upon the evaluation.

It is noticed that only the TLI and MTI are studied, except ITI, which has experienced instability, as discussed in section III.

5.1. Simulation Speeds

The multi-rate simulation was carried out in Advanced Digital Power System Simulator (ADPSS). The original test system is divided into 6 subsystems. 5 DC subsystems with the boundary of grey dash lines are simulated with a small time-step, 2μs. One AC subnetwork with the boundary of deep dash lines is simulated with a large time-step, 50μs, as shown in Fig.16.

The detailed comparison of different dividing schemes is illustrated in Fig. 18. The label in the axis of X represents the division type, (number of AC subsystems + number of DC subsystems). The PSCAD software runs about 1860s for 1s period of simulation as the computation time base. When the system is divided into a fast system (DC) and a slow system (AC), the computation time for 1s period of simulation decreases to 273s using the TLI or 294s using the MTI. Thus, the rate of speed-up reaches 6.33 (1860/294) for MTI. When the system is divided into more subsystems with small time-steps, a higher rate of speed-up is obtained, which can be up to 64. However, if there are more than 6 subsystems, the speed of simulation improves insignificantly through increasing subsystems with small time-steps. Therefore, the dividing scheme is optimized, which has 5 DC subsystems and 1 AC subsystem.

When using multi-rate algorithms for real-time simulation, the costs of hardware, e.g. racks, processors or FPGAs, can be greatly
reduced for simulating large systems with more power electronics converters and with smaller time steps [34].

Fig. 17 System Topology of Zhou Shan MTDC.

Fig. 18 Computation Time Comparison of different subsystem division types.

5.2. Accuracy Evaluation

The accuracy evaluation is to compare simulation results between non-partitioned system with a single time-step of 2μs and the partitioned system with different rates of 2μs and 50μs, as shown in Fig.19-20.
In Fig 19 to 20, the first row represents the DC voltage of CS1, and the second row is the AC voltage of CS2. The errors expressed as a percentage are analyzed in frequency domain. It shows that all of errors are less than 1%. And the errors in the fundamental frequency are less than 0.03%. Comparing errors of the TLI and the MTI, the MTI has smaller than half of the errors of the TLI, thus the MTI is more accurate than the TLI.

6. CONCLUSION

With wide applications of IGBT-based power electronic devices in modern power systems, multi-rate simulation, as an expecting simulation technology, shows necessity for the simulation of large-scale power systems. This paper addresses the difficulties of using the multi-rate simulation.

The key findings of the study include

1. An improved method, Modified Thevenin Interface (MTI) is proposed to overcome the drawbacks in synchronization of the original Thevenin Interface.

2. Three theorems about ITI, TLI and MTI are proposed and proved in theoretically. From these theorems, it has been found that the stability of multi-rate simulation using these interfaces is only associated with the stability of single rate parallel simulation using these interfaces. The stability of MTI is only associated with the stability of the simulated case. Therefore, MTI performs higher stability and less limitation.

3. In terms of error analysis, the errors of ITI only depend on the delay introduced by the parallel algorithm, which are smaller than the errors of TLI.

4. The errors of TLI are dependent on the associated capacitor of TLI and the equivalent admittance of the subsystem from the
5. The errors of the MTI are convergent and much smaller than the previous two.

Through the proposed theorems and methods, the accuracy and stability of the multi-rate parallel simulation of DC grids are able to be evaluated conveniently. Multi-rate simulation has been performed to analyze the practical project, Zhoushan MTDC in China, which presents high speed and accuracy. The suitable multi-rate simulation algorithm can be applied in the analysis of DC grids to achieve fast and accurate simulation results.

7. REFERENCES


8. APPENDIX

8.1. Parameters of Zhoushan MTDC

Some parameters of VSC-MTDC in Fig. 17 on the large case study are listed as below, the detailed information refers to [33].

Transmission Lines: Line 1=46km, Line 2=17km, Line 3=39km, Line 4=32.3km R=0.0192ohm/km, L=0.24mH/km, C =0.2961uF/km.

Converters:

Zhoushan (CS1) (VQ mode), V=±200kV, Q=60MVar Capacitor=432uF; Leakage reactance= 14.098%; MVA rating = 450MVA
Daishan (CS2) (PQ mode), P=-200MW, Q=60MVar; Capacitor= 432uF; Leakage reactance= 14.98%; MVA rating = 350MVA
Qushan (CS3) (PQ mode), P=80MW, Q=20MVar; Capacitor= 108uF; Leakage reactance= 14.8%; MVA rating = 120MVA
Yangshan (CS4) (PQ mode), P=80MW, Q=20MVar; Capacitor= 144uF; Leakage reactance= 14.8%; MVA rating = 120MVA
Shensi (CS5) (PQ mode), P=80MW, Q=20MVar; Capacitor= 144uF; Leakage reactance= 14.8%; MVA rating = 120MVA

Topology of Converters:

Controller parameters for CS1 to CS5:

Id/Iq inner Loop Ti= 0.1075 Kp= 0.6907; Ed Outer loop Ti=0.2 Kp=2; Vac Outloop Ti=0.4 Kp=2;

Filter: R=10Ω L=3H

8.2. Parameters of Power Systems in ZhouShan MTDC

Infinite Bus:

<table>
<thead>
<tr>
<th>Vbase</th>
<th>Vac</th>
<th>Phase</th>
<th>R</th>
<th>L</th>
</tr>
</thead>
<tbody>
<tr>
<td>230kV</td>
<td>0.991244p.u.</td>
<td>-26.415037</td>
<td>0.002p.u.</td>
<td>0.02p.u.</td>
</tr>
</tbody>
</table>

AC Transformer:

<table>
<thead>
<tr>
<th>Station</th>
<th>MVA rating</th>
<th>Voltage</th>
<th>Leakage Reactance</th>
<th>Resistance</th>
</tr>
</thead>
<tbody>
<tr>
<td>Liangjia</td>
<td>360MW</td>
<td>236/20</td>
<td>0.018610*2</td>
<td>0.000295*2</td>
</tr>
<tr>
<td>Penglai</td>
<td>180MW</td>
<td>230/230</td>
<td>0.041950*2</td>
<td>0.000515*2</td>
</tr>
</tbody>
</table>
### Load Condition

<table>
<thead>
<tr>
<th>Station</th>
<th>Rating</th>
<th>Voltage (p.u.)</th>
<th>Active Power (p.u.)</th>
<th>Reactive Power (p.u.)</th>
<th>MVA Base</th>
<th>kV Base</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZhouBei</td>
<td>0.9715</td>
<td>0.6195</td>
<td>0.1405</td>
<td></td>
<td>100</td>
<td>230.00</td>
</tr>
<tr>
<td>LiangJia</td>
<td>1</td>
<td>0.21</td>
<td>0.1</td>
<td></td>
<td>100</td>
<td>20.0</td>
</tr>
<tr>
<td>Penglai_115</td>
<td>0.94335</td>
<td>0.3280</td>
<td>0.16400</td>
<td></td>
<td>100</td>
<td>37.0</td>
</tr>
<tr>
<td>Penglai_B</td>
<td>0.961</td>
<td>0.47710</td>
<td>0.15760</td>
<td></td>
<td>100</td>
<td>115</td>
</tr>
<tr>
<td>Shengjia</td>
<td>0.9804</td>
<td>0.2301</td>
<td>0.2454</td>
<td></td>
<td>100</td>
<td>115</td>
</tr>
<tr>
<td>Qushan</td>
<td>0.9638</td>
<td>0.20530</td>
<td>0.0492</td>
<td></td>
<td>100</td>
<td>115</td>
</tr>
<tr>
<td>Shensi</td>
<td>0.97125</td>
<td>0.2616</td>
<td>0.4013</td>
<td></td>
<td>100</td>
<td>115</td>
</tr>
</tbody>
</table>

### Gen Model (LiangJia GEN)

<table>
<thead>
<tr>
<th>Power Rating</th>
<th>300MW</th>
<th>V RMS</th>
<th>20kV</th>
<th>Stator resistance</th>
<th>0.001p.u.</th>
<th>Leakage inductance</th>
<th>0.174</th>
<th>Inertia constant</th>
<th>5.269s</th>
</tr>
</thead>
<tbody>
<tr>
<td>Xd(p.u.)</td>
<td>1.86</td>
<td>Xd'(p.u.)</td>
<td>0.229</td>
<td>Xd''(p.u.)</td>
<td>0.174</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Xq(p.u.)</td>
<td>1.75</td>
<td>Xq'(p.u.)</td>
<td>0.382</td>
<td>Xq''(p.u.)</td>
<td>0.174</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Td0'</td>
<td>8.6s</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Tq0'</td>
<td>0.96s</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### Governing and Turbines (Type 1)

<table>
<thead>
<tr>
<th>Valve Position Up limit</th>
<th>1</th>
<th>Valve Position Down limit</th>
<th>0</th>
<th>Opening Rate Limit</th>
<th>1.0</th>
<th>Closing Rate Limit</th>
<th>-1.0</th>
<th>Dead band</th>
<th>0.001</th>
<th>Speed Relay</th>
<th>0.1s</th>
<th>Coefficient of Speed governor</th>
<th>20</th>
<th>Time constant of main inlet volumes and steam chest</th>
<th>0.2s</th>
<th>Time constant of reheater</th>
<th>10s</th>
<th>Fraction of HP</th>
<th>0.333</th>
</tr>
</thead>
</table>

### Excitation Systems (Type 1)

| Amplifier | 10 | Time constant | 0.01s | Maximum of field voltage | 21.5p.u. | Minimum of field voltage | -21.5p.u. | Time constant of excitation | 0.1s | Proportional coefficient of excitation | 1.0 | Coefficient of Speed governor | 20 | Compensation Coefficient | 0.001 |

### Line Parameters (MVA Base = 100MVA)

Where CZ is the abbreviation of Z_Changzhou, YD is abbreviation of Z_Yidu, PL is the abbreviation of Z_Penglai, ZB is the abbreviation of Zhoubei, LJ is the abbreviation of Z_Liangjia, CS represents the converter station, SJ is the abbreviation of Shengjia, QS is the abbreviation of Z_Qushan, SS is the abbreviation of Z_Shensi.

### 8.3. Parameters of the unstable case study

#### Parameters of the circuit

<table>
<thead>
<tr>
<th>R_1</th>
<th>5000Ω</th>
<th>R_2</th>
<th>1Ω</th>
</tr>
</thead>
<tbody>
<tr>
<td>R_3</td>
<td>4Ω</td>
<td>R_4</td>
<td>-0.76Ω</td>
</tr>
<tr>
<td>R_{in}</td>
<td>0.1Ω</td>
<td>L</td>
<td>0.3H</td>
</tr>
<tr>
<td>L_{2}</td>
<td>0.03H</td>
<td>L_{111}</td>
<td>0.2H</td>
</tr>
<tr>
<td>V_0</td>
<td>sin(1^{°}\pi t V)</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Highlights

- Three theorems for the stability of three typical multi-rate simulation algorithms have been proposed and proved.
- The error of three typical multi-rate simulation algorithms has been modelled.
- The backward synchronization for the Thevenin equivalent interface has been proposed to overcome the challenge of synchronization.
- Three typical multi-rate simulation algorithms have been compared to illustrate that the modified Thevenin equivalent interface is the most stable and accurate.
- The multi-rate simulation algorithm has been used in the studies of DC grids, which achieve the great improvement of the simulation speed.