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## Growing InGaAs quasi-quantum wires inside semi-rhombic shaped planar InP nanowires on exact (001) silicon

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We report InGaAs quasi-quantum wires embedded in planar InP nanowires grown on (001) silicon emitting in the 1550 nm communication band. An array of highly ordered InP nanowire with semirhombic cross-section was obtained in pre-defined silicon V-grooves through selective-area heteroepitaxy. The 8% lattice mismatch between InP and Si was accommodated by an ultra-thin stacking disordered InP/GaAs nucleation layer. X-ray diffraction and transmission electron microscope characterizations suggest excellent crystalline quality of the nanowires. By exploiting the morphological evolution of the InP and a self-limiting growth process in the V-grooves, we grew embedded InGaAs quantum-wells and quasi-quantum-wires with tunable shape and position. Room temperature analysis reveals substantially improved photoluminescence in the quasi-quantum wires as compared to the quantum-well reference, due to the reduced intrusion defects and enhanced quantum confinement. These results show great promise for integration of III-V based long wavelength nanowire lasers on the well-established (001) Si platform. *Published by AIP Publishing*. [http://dx.doi.org/10.1063/1.4953839]

Monolithic integration of efficient and reliable on-chip light source on silicon remains an impediment of silicon photonics development.<sup>1</sup> Recent advances in III-V long wavelength lasers directly grown on silicon highlight their technological importance to optical interconnects.<sup>2–5</sup> Growing III-V planar nanostructures on patterned silicon substrates utilizing the aspect ratio trapping (ART) technique is a promising approach to develop electronic and photonic integrated circuits that incorporate optical emitters. This approach demonstrates the advantages of using large diameter industrial-standard silicon substrates, and good compatibility with the well-established silicon planar processing platform. Furthermore, ART growth could eliminate the need of thick buffers for low defect mismatched layers by employing epitaxial defect necking effect. To date, various III-V nanowires,<sup>6,7</sup> ridge quantum wells (QWs),<sup>8,9</sup> GaAs thin films free of anti-phase domains,<sup>10</sup> and InP distributed feedback laser array monolithically grown on (001)-silicon<sup>4</sup> have been demonstrated.

In this letter, we report InGaAs quasi-quantum wires (quasi-QWRs) embedded in semi-rhombic InP nanowire arrays grown on exact (001) Si substrates using the ART method. The quantum confinement of quasi-one-dimensional electrons in quantum wires renders them attractive for applications in optical modulators, optical switches, and, particularly, semiconductor lasers.<sup>11,12</sup> Reported research of QWRs includes selective growth on vicinal surface,<sup>13</sup> edge QWRs by cleavage,<sup>14</sup> and growth at the bottom of V-grooves or at the top of ridge structure.<sup>15–18</sup> However, these QWRs exclusively grown on III-V substrates were often accompanied by the simultaneous formation of side quantum wells (QWs).

In this work, we demonstrate planar InP nanowires on Vgrooved (001) silicon using an ultra-thin stacking disordered nucleation layer to overcome the large lattice mismatch. InGaAs quantum structures were inserted inside the InP nanowires at varied positions. Good crystalline quality of the InGaAs quasi-QWRs near the tip of the InP nanowires and the absence of InGaAs side quantum-wells was revealed by transmission electron microscope (TEM) inspection. This allows for direct probe of the optical properties of the quasi-QWRs using room temperature photoluminescence (PL) measurement. Emission from the InGaAs quasi-QWRs at the 1550 nm band indicates great potential of fabricating long wavelength III-V nanowire lasers on the mature Si platform.

The initial Si substrates were patterned with 150 nm thick  $SiO_2$  stripes along the [110] direction with a line pitch of 800 nm and 300 nm openings. After a standard RCA-1  $(NH_4OH:H_2O_2:H_2O = 1:1:5)$  cleaning process and a brief HF dip to remove native oxide, the samples were immediately immersed in a KOH (45% at 70 °C) solution to form V-groove pockets enclosed by four Si (111) surfaces. Epitaxial growth was conducted in a metal-organic chemical vapor deposition (MOCVD) system with a horizontal reactor (AIXTRON 200/ 4). Triethylgallium (TEGa), tertiarybutylarsine (TBA), trimethylindium (TMIn), and tertiarybutylphosphine (TBP) were used as growth precursors and H<sub>2</sub> as carrier gas. After an in-situ thermal cleaning process at 800 °C in H<sub>2</sub> ambient for 15 min, a low temperature GaAs nucleation layer was deposited at 400 °C with a V/III ratio of 12, followed by InP nucleation deposited at 450 °C with a V/III ratio of 28. Afterwards, the reactor temperature was ramped up to 550 °C, and the bottom half of the Si pockets were filled up by growing InP at a V/III ratio of 17. Finally, the rest InP epilayer was grown at 600 °C with a V/III ratio of 34, during which InGaAs quantum structures were inserted.

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Fig. 1(a) shows the  $70^{\circ}$  tilted-view SEM image of the planar as-grown InP nanowires separated by SiO<sub>2</sub> stripes. Highly ordered and uniform InP nanowires with semirhombic cross-section have been obtained. The zoomed-in SEM image in Fig. 1(b) shows smooth growth facets consisting of two convex (111) B surfaces and a flat (001) surface on the top. We analyzed the epitaxial quality of the InP nanowires by high-resolution x-ray diffraction (HRXRD) measurement using an Empyrean system. The (004) plane diffraction peak of the InP nanowires, with a full-width-athalf-maximum (FWHM) of 234 arc sec, is plotted with the one from the Si substrate in the  $\omega$ -2theta curve (Fig. 2). To further assess the defect level,  $\omega$ -rocking curves across the InP peak was measured, with the line-focused x-ray beam in aligned orientations perpendicular or parallel to the nanowires. As shown in the inset of Fig. 2, FWHM of 659 and 731 arc sec were obtained from the parallel and perpendicular direction, respectively. These FWHM values are comparable to that measured from  $1.8 \,\mu\text{m}$ -thick InP thin films on planar GaAs-on-Si substrates<sup>19</sup> and slightly larger compared to GaAs<sup>20</sup> and InP<sup>7</sup> on V-grooved Si using a higher aspect ratio nano-pattern.

High resolution TEM was performed to investigate the crystalline quality and the defect trapping mechanism in the InP nanowires. We prepared TEM specimens using conventional mechanical polishing and ion beam milling process. A JEOL2010F field-emission microscope operating at 200 keV was used for the TEM observation. TEM images were taken along the [110] zone axis under multiple-beam conditions. We intentionally tilted the TEM specimen to achieve the best contrast to view the defects. Fig. 3(a) presents a crosssectional TEM image of a representative InP nanowire. A majority of the defects generated at the III-V/Si interface are confined within the V-grooved pocket, with a few visible dislocations and stacking faults propagating into the bulk of the InP wire and ending at the (111) InP surface. As a result, better crystalline quality is obtained near the tip of the InP rhombic nanowire. The high resolution TEM image in Fig. 3(b) reveals a rounded growth front of the nanowire tip with a transitional (113) surface connecting the top (001) and side (111) B surfaces. The formation of these non-flat facets is due to the interfacial energy of the SiO<sub>2</sub> sidewall and the inclination to minimize total surface energy during the selective heteroepitaxy process. Fig. 3(c) presents a zoomed-in TEM image taken at the InP/GaAs/Si interface. Stacking faults in parallel with the growth surface were formed during the low-temperature-deposited InP/GaAs nucleation buffer to accommodate the 8% lattice mismatch. This stacking disordered layer of  $\sim 15$  nm in thickness terminates at the corner under the edge of the  $SiO_2$  stripe, as indicated by the yellow line in Fig. 3(a). A close-up of the III-V/Si interface manifests the critical role of the GaAs nucleation layer in the subsequent InP growth (Fig. 3(d)). Merckling et al. reported direct nucleation of InP on V-grooved Si by lowering the growth temperature of the InP nucleation layer to 370°C together with using a V/III ratio of larger than 2000.<sup>7</sup> However, it is difficult to deploy such extreme growth conditions for optimized crystalline quality. In our studies, growing InP in Si V-grooves without the GaAs nucleation layer resulted in large and sparsely deposited InP clusters. Another study reported by Li et al. employed thick GaAs buffer filling up the entire V-grooved pockets.9 InP was deposited on the flat (001) GaAs surface subsequently. However, such an approach undermines the unique defect trapping capability of Si V-grooves. In this work, we initiated the III-V/Si hetero-epitaxy with GaAs to promote uniform InP coverage on the Si surface. The GaAs layer was kept extremely thin so that defect trapping benefits of growing InP on (111) GaAs/ Si can be preserved. The lattice mismatch between InP and GaAs was accommodated by the formation of a few nanometer-thick stacking faults that ran parallel to the (111) Si surfaces instead of threading dislocations that would propagate upward.

We grew InGaAs quantum-well and quasi-quantumwire structures at various positions inside the rhombic InP nanowires by tuning the insertion time of the InGaAs layer. Three samples (QW-1, QWR-1, and QWR-2) have been prepared using the same gas flow rates. Cross-sectional TEM images in Fig. 4 show the morphological evolution from quantum-well to quasi-quantum-wire. In sample QW-1 (Fig. 4(a)), we grew an InGaAs layer flush with the V-grooved Si pocket for 60 s. A 10 nm flat InGaAs quantum well with uniform thickness across the pocket opening is clearly identified in the zoomed-in TEM image in Fig. 4(b). Dislocations penetrating the quantum well and terminating at the (111) InP surface are also observed. In sample QWR-1 (Fig. 4(c)), growth of the InGaAs layer has been postponed until the formation of {111} side facets. A crescent-shaped quasi-QWR instead of flat QW is observed. The zoomed-in TEM image in Fig. 4(d) reveals that the quasi-QWR is 20 nm thick and  $\sim$ 130 nm wide. It is worth noting that the same growth time (60 s) with QW-1 was used for the growth of QWR-1, yet the growth rate has been doubled due to the reduced area on the  $\{001\}$  plane. The TEM image in Fig. 4(d) also details that



FIG. 1. (a)  $70^{\circ}$  tilted SEM image of as grown highly ordered InP nanowires separated by SiO<sub>2</sub> spacers; (b) zoomed in tilted SEM image showing the semirhombic cross-section and flat (111) and (001) InP facets.



FIG. 2. Typical HRXRD  $\omega$ -2theta scan of the InP nanowires grown on Vgrooved Si (the Si (004) and InP (004) peaks are indicated). The inset compares  $\omega$ -rocking curves measured from directions perpendicular and parallel to the InP nanowires.

the quantum wire is wrapped up by thin ( $\sim 10$  nm) InP on the left and the right sides and 60 nm InP on the top. The InGaAs thickness on the {111} facets is less than 1 nm, indicating a large growth preference on the {001} surface. As growth proceeds, we expect a further decrease of the {001} surface, accompanied with an increase of nanowire tip curvature. Fig. 4(e) displays the TEM image of sample QWR-2, in which the InGaAs quantum wire was placed further up towards the tip region of the InP wires. To compensate for the enhanced growth rate, the growth time of the InGaAs layer was reduced to 9 s. As a result, we were able to shrink the quantum-wire dimension in both vertical and lateral directions. As shown in the zoomed-in image in Fig. 4(f), a vertical thickness of 13 nm and a lateral width of ~90 nm are



FIG. 3. (a) Cross-sectional TEM image (perpendicular to the trench direction) of a single semi-rhombic InP nanowire with labeled regions; (b) zoomed in TEM image showing the three facets at the tip of the InP nanowire: (111) B, (113), and (001); (c) cross-sectional TEM image at the InP/ GaAs/Si interface, indicating the formation of stacking faults along the direction of the Si surface at initial nucleation stage; and (d) high resolution TEM image of the III-V/Si interface.



FIG. 4. (a) and (b) Cross-sectional TEM image showing the formation of the InGaAs quantum well flush with the SiO<sub>2</sub> separating the V-grooved pockets, and zoomed-in TEM image measuring the 10 nm thick InGaAs. (c) and (d) Cross-sectional TEM showing the formation of quasi-quantum wire near the top of InP nanowire and zoomed-in image indicating a 20 nm thick InGaAs QWR wrapped up by 10 nm InP at lateral direction and 60 nm InP at vertical direction. (e) and (f) High-resolution TEM image showing quasi-quantum wire with better quantum confinement at both lateral and vertical directions. The 13 nm thick InGaAs QWR is wrapped by 34 nm InP laterally and 50 nm InP vertically. EDX measurement points are also labeled.

obtained in QWR-2. The surrounding InP barrier has a thickness of 34 nm on the sides and a vertical thickness of 56 nm on the top.

The TEM images in Fig. 4 reveal a high degree of InGaAs thickness inhomogeneity in the quasi-QWR samples. We have further characterized the indium composition using energy-dispersive X-ray spectroscopy (EDX) from the edge to the center of the quasi-QWR in QWR-2. The measured positions are marked in Fig. 4(f). As shown in Fig. 5, indium fraction peaks at the center region of the InGaAs well and decreases as the measurement point moves to the sides. Such spatial distribution could be due to the diffusion of indium adatoms from the high curvature region (the transitional facets) to the flat region (in this case, (001) facets) due to a large diffusion coefficient.<sup>21</sup> We therefore expect there is a lateral quantum confinement effect at the central region from the edge directions.

The optical quality of both the quantum-well and the quasi-quantum-wires was characterized by room temperature



FIG. 5. Average indium fraction at different position of QWR-2 from EDX.

PL measurement using a 671 nm laser and a Ge detector. The excitation power was 300 mW, and the laser spot size was around 100  $\mu$ m in diameter. The PL spectra of the three samples measured at full laser power are plotted in Fig. 6 for comparison. Sample QW-1 exhibits a central emitting wavelength of 1516 nm and a FWHM of 168 nm. A five-fold enhancement in the peak intensity is achieved in QWR-1 as compared with QW-1, along with an emitting wavelength of 1512 nm and a FWHM of 170 nm. Combined with better confinement, enhanced material quality at the upper region of the InP nanowire and reduced likelihood of defect penetration in the quantum wire led to the substantial improvement in PL emission. From QWR-1 to QWR-2, the peak PL intensity was almost doubled. The central emitting wavelength of QWR-2 was slightly shifted to a shorter wavelength of 1480 nm, and a FWHM of 184 nm was obtained. To investigate the dominant mechanism behind the PL improvement, power-dependent PL measurements were performed and the results are shown in the inset of Fig. 6. Comparing the integrated PL intensity of the flat quantum well with that of the quasi-quantum wires, the difference in PL intensity decreases slightly with increasing pumping power. This reflects the influence of defect density. The lower the pump power, the more sensitive of PL intensity to defects.<sup>22</sup> Comparing the two QWR samples, the difference in the PL intensity increases somewhat as the pumping



FIG. 6. Room temperature PL measurement of the three samples: QW-1, QWR-1, and QWR-2. The inset shows power dependent measurement PL of the three samples.

power density increases. As the position of both QWR-1 and QWR-2 are near the top tip of the InP nanowire, we expect there is little difference in the defect densities. Enhanced quantum confinement effect in the vertical direction in QWR-2 should take most of the credit for the PL improvement (QWR-1 thickness is 20 nm and QWR-2 thickness is 13 nm).

In conclusion, we have demonstrated the growth of InGaAs quasi-QWRs embedded in the highly ordered InP semi-rhombic nanowires on exact (001) Si substrates. Vgrooved pockets with four (111) Si interfaces were utilized to trap the defects generated at the III-V/Si interface and a mere 10 nm thick GaAs interlayer was used to accommodate the 8% lattice difference between InP and Si. High quality InP nanowires with rhombic cross-section and flat facets were achieved as evidenced by SEM and HRXRD measurements. Inserting InGaAs layers at different phase of the growth led to the formation of QWs and quasi-QWRs. The high crystalline quality and optical property of these InGaAs quantum structures were examined through TEM and PL measurements. The evolution of these quantum structures was accompanied by PL enhancement and a slight blue shift of the central emitting wavelength.

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