Growth of quantum dot coated core-shell anisotropic nanowires for improved thermal and electronic transport

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ABSTRACT

Anisotropic nanowires are promising candidates for electronic thermal management due to their unique electrical and thermal properties. However, eco-friendly solution-processed nanomaterials with an elaborate morphology and microstructure for modulating thermal and charge transfer are still a considerable challenge. Herein, we present a simple but effective approach for synthesizing pseudo core-shell nanowires through quantum dot (QD)-like nanostructure coating (*p*-NW@QD) to generate exceptional electron-phonon transport properties. With the assistance of diphenyl ether as a coordination solvent, high crystallinity lead sulfide NWs can be fabricated with a large aspect ratio together with uniform QD coating. This *p*-NW@QD exhibits high electronic mobility (30.65 cm²/Vs) as well as a diameter independent low thermal conductivity (1.53 ± 1 W/m K). Direct charge/heat carrier flow measurements and computational simulations demonstrate that the unusual electrical and thermal transport phenomenon is strongly dependent on the fast charge transport through the QD shell, and a slow phonon migration across the Umklapp process dominated NW cores. These findings indicate a significant step toward colloidal synthesis nanostructures for future high-performance nanoelectronics and thermal energy devices.

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Thermal transport in materials is drawing persistent attention because of intriguing fundamental thermal physics in various dimensions, as well as the growing importance of heat management in electronic devices and electricity base.^{1,2} For instance, thermal transport can create an uneven distribution of charge carriers in semiconductors (e.g., thermoelectric effect), which could provide a global sustainable energy solution by generating electricity from waste heat.^{1,3,4} The fundamental principle underlying the thermal transport is the flow of electrons and phonons that carry heat and charge under a thermal gradient.³ In semiconductors, this dynamic process has been theoretically rationalized as $ZT = \sigma S^2 T / (\kappa_{el} + \kappa_{lat})$ (i.e., Peltier–Seebeck Effect), where ZT is the dimensionless figure of merit, σ is the electrical conductivity, T is the temperature, S is the Seebeck coefficient, and κ_{lat} and κ_{el} are the lattice and electronic thermal conductivities ($\kappa = \kappa_{el} + \kappa_{lat}$).³ Realizing a low thermal conductivity but high electrical conductivity is the figure-of-merit for achieving a temperature gradient in a conductive substance, which is critical for electronically thermal energy management, such as thermoelectric generators (TE).^{5,6} Unfortunately, *S*, σ , and κ are interdependent variables, and thus, they cannot merely be tailored.²

In low-dimensional nanostructures, the anisotropic spatially confined density-of-states (DOS) can provide a solution to alter one of the interrelated "Peltier–Seebeck Effect" parameters without affecting the others [Fig. 1(a)].⁷ Recently, various approaches have been explored on the development of nanoscale structures for controlling electron-phonon interactions and transports including Fermi liquid,⁵ insulator,⁸ alloys,⁹ or doped semiconductors.¹⁰ In theory, the carrier transport (including charge and heat) can be controlled through dimension or compositional-lattice engineering.^{7,11} However, in order to generate a *ZT* value that is large enough (e.g., above 3) to compete with conventional power generators,¹² the diameter of the nanostructures, such as nanowires (NWs),^{13–16} needs to be extremely small (e.g., below 10 nm), which challenges the limitation of the state-of-the-art nanofabrication.¹⁴

Nevertheless, the seminal studies in Si or Ge NWs introduced a "surface phonon scattering" (SPS) strategy via NW surface modification, which showed significant improvement in TE performance.^{11,15,17} SPS has shown promise in altering the electrical and thermal transport, but this controllability can be significantly enhanced if fundamental differences between semiconductors such as bandgaps and exciton binding energies could be considered.^{2,6,12,14} For example, the exciton Bohr radius of lead sulfide (PbS) is three times larger than that of Si, and PbS has shown a strong quantumconfinement effect in manipulating charge-mobility in various optoelectronic and photonic devices.^{18–20} Therefore, in addition to SPS, an electronic quantum-confinement can independently influence and contribute to tuning the thermal transport (i.e., TE power factor) if we modify the NW surface into a spatially confined structure.^{4,7,14,17,21}

Herein, we present an approach for synthesizing pseudo coreshell nanowires with a quantum dotlike nanostructure coating (*p*-NW@QD) through a solvent-mediated approach. With the assistance of a coordinating reagent diphenyl ether (DER), NWs not only grow along the $\langle 112 \rangle$ direction but also can be uniformly coated with highly crystalline QDs. We further fabricate field-effect transistors (FETs) and a microfabricated suspended heating device to directly probe charge/heat flow dynamics within a single PbS *p*-NW@QD. Based on theoretical simulations and systematic microscopy analysis, we determine that the anomalous low thermal conductivity and high charge carrier mobility originate from *p*-NW@QD, efficiently regulating the charge and phonon transport via QD shells and NW cores, respectively.

To facilitate the anisotropic crystal growth, we combined the oriented attachment methodology with a solvent-mediated crystalgrowth approach to fabricate the *p*-NW@QD core-shell nanostructure. As illustrated in Figs. 2(a)-2(c), we found that DER can play an important role in tuning the surface morphology besides its conventional role as a precursor solvent.^{22,23} As shown in Fig. 2(a), the morphology of the as-prepared NWs was dramatically changed when the coordinating-solvent DER was introduced. As indexed in the selectedarea electron diffraction (SAED) patterns and powder X-ray

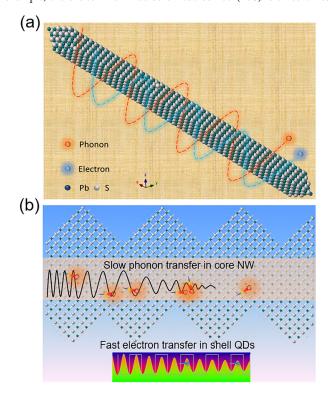


FIG. 1. (a) A schematic description of electronic and phonon transport in PbS NWs. (b) Cross-section schematic of the as-proposed *p*-NW@QD shows the "thermoelectric rectification" mechanism that enables slow phonon propagation within the NW "core" and fast electron transfer through the QD "shell."

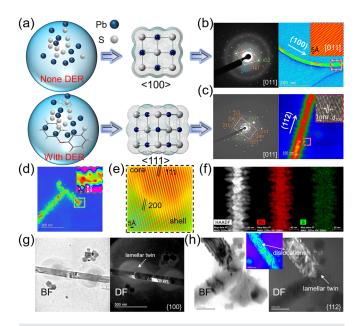


FIG. 2. (a) Schematic illustration of the synthesis process of PbS *p*-NW@QD coreshell structures. (b)–(e) SAED, ARM, HAADF-STEM, and HRTEM images of the as-prepared PbS NWs and *p*-NW@QDs in the contrast color scale. The lattice spacings of {200} and {111} planes were 3.0 ± 0.1 Å and 3.4 ± 0.2 Å, respectively. (f) HAADF-STEM elemental mapping images of *p*-NW@QDs. (g) and (h) BF and DF images highlight the dislocations and lamellar twin defects in greyscale and contrast color scales (inset).

diffraction (XRD) patterns (Fig. S1), the addition of DER in the NW growth media [1-octadecene (ODE):DER = 10:1/v:v] can effectively adjust the growth direction of NWs from (100) [Fig. 2(b)] to (112)[Fig. 2(c)]. DER has higher nucleophilic nature compared to ODE. Therefore, it can be anticipated that DER would be bound to the Pb ions of the growing nanocrystals, similar to the other amines (i.e., oleylamine, trioctylamine, hexadecylamine, etc.) to control the morphology and growth direction.^{22,23} The as-prepared NWs and p-NW@QDs exhibited a high degree of crystallinity [insets of Figs. 2(b), 2(c), and S2) with diameters between 50 nm and 500 nm and lengths up to 10 um. As shown in Fig. 2(d), the QD-like nanograins (dimension 5-15 nm) formed spatially confined nanostructures [inset of Figs. 2(d) and S3] and uniformly distributed on the surface of NWs. Moreover, as shown in the false-color bright-field (BF) microscopy images [Figs. 2(d) and 2(e)], we observed an enrichment of structuraldisorder in (112) oriented *p*-NW@QDs. As further highlighted in the atomic-resolution microscopy (ARM) aberration-corrected high-angle annular dark-field (HAADF-STEM) images, moiré fringe defects from the lattice mismatch {200 and 111} and twinnings can be readily resolved at the $\langle 112 \rangle$ oriented NW/QD core and shell interface [Fig. 2(e)].²² The composition of the as-prepared *p*-NW@QDs was determined through X-ray photoelectron spectroscopy (Fig. S4) and HAADF-STEM elemental-mapping [Fig. 2(f)], which showed that a homogeneous Pb-S distribution excluded the possibility of the presence of localized-sediments.9,24,25 We further probed the structural difference between $\langle 100 \rangle$ and $\langle 112 \rangle$ oriented NWs through direct-beam BF and diffracted-beam dark-field (DF) imaging. As displayed in Fig. 2(g), only twinning dislocations and lamellar twin boundaries can be resolved in $\langle 100 \rangle$ oriented NWs (Fig. S5). On the other hand,

twinning, dislocations, and stacking fault bend contours can be identified in the thickness and stacking-fault fringes among the $\langle 112 \rangle$ preferred *p*-NW@QDs [Figs. 2(h) and S6]. The large aspect ratio (~100), disordered lattice structure, and quantum-confined surface shell are expected advantages to give rise to exceptional thermal and charge transport properties within this *p*-NW@QD core-shell structure.

To gain further understandings on the dynamical thermal transport in PbS NWs under a thermal gradient, we carried out systematic density functional perturbation theory (DFPT) and molecular dynamics (MD) thermal transport simulation. As shown in Fig. 3(a), the strong anharmonicity of PbS is reflected in the dominated acoustic phonons from its phonon density of state (PDOS) as calculated from the DFPT.^{26,27} This strong anharmonic characteristic leads to large Grüneisen parameters and large phonon scatterings which were determined to be the main reason for the intrinsic low lattice thermal conductivity in PbS crystals (e.g., 2.5 W/m K at 300 K).^{26,27} Atomic configurations of the PbS system and the steady-state nonequilibrium molecular dynamics (NEMD) method setup are illustrated in Fig. 3(b) and the supplementary material.²⁸ After the NEMD calculation, a steady-state temperature gradient was built along the heat flux direction [Fig. 3(c)].^{29,30} As shown in Fig. 3(d), within the temperature range of 200 K-400 K, the phonon-free-path threshold value can be estimated to be 9.6 nm which is close to previous predictions based on first-principles calculations.²⁶ As shown in Fig. 3(e), when two incoming phonons (vector k_1 and k_2) scatter inside of the first Brillouin zone, the derived phonon k_{3}' may constrain inside or outside via the normal-scattering-process (N-process) and the Umklapp-process (U-process).³¹ The U-process suppresses the thermal conductivity which was reflected through exponential κ decay as a function of temperature

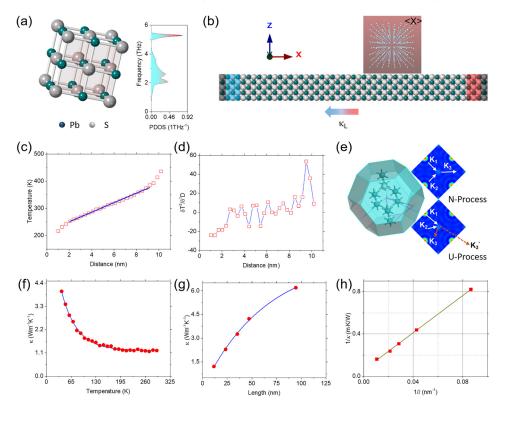


FIG. 3. (a) Left panel: standard cell of PbS shows a rock-salt cubic structure. Right panel: calculated phonon density-ofstates. (b) Atomic configurations used in the PbS nanowire MD simulations. The outermost atom layers denoted in black shades are fixed. The blue and red shaded areas stand for the heat sink and heat source, respectively. (c) Temperature distribution along the heat flux direction in the steady state. (d) Secondary-derivative length-dependent thermal distribution for estimating the length of the phonon free pathway, and D refers to the distance. (e) The first Brillouin zone for the primitive cell of PbS and two common phonon scattering processes. (f) Temperature dependence of thermal conductivity. (g) Dependence of thermal conductivity with the system length from 11.6 to 94.5 nm. The blue dashed line is plotted for visual aid. (h) The inverse relationship of $1/\kappa$ and 1/l. Linear fitting is employed to extrapolate the thermal conductivity of infinite length PbS.

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[Fig. 3(f)]. Interestingly, we revealed that the predicted thermal conductivities showed a strong dependence on the system length (1) when the system dimension is comparable to or smaller than the material intrinsic phonon-mean-free-path (MFP).³² In general, the thermal transport will become ballistic at small length scales when l < MFP, and within the ballistic regime, certain phonon modes can transmit from the heatsource to the heat-sink without scattering. As shown in Figs. 3(g) and 3(h), the calculated κ increases from 1.2 W/m K to 6.2 W/m K when the l value increases from 11.6 nm to 94.5 nm which is close to the predicted values of 4.14-4.29 W/m·K from first principles calculations.³ This suggests that a low-dimensional nanostructure is generally beneficial for suppressing the thermal conductivity, but a less phonon quantum-confinement impact could be expected unless the dimension is reduced below 10 nm. Therefore, we focus on improving the charge mobility through quantum-confinement while retaining the intrinsic low thermal conductivity to advance thermal transport modulation.⁴

To understand the effect of QD shells for enhancing electronic transport, we evaluate the carrier mobility of the as-prepared p-NW@QDs through fabricating single-wire field-effect transistors (FETs). Figure 4(a) shows a scanning electron microscopy (SEM) image of the fabricated back-gated PbS NW FET with four electrodes (Fig. S7). The source-drain current-voltage (I_{ds}-V_{ds}) plot [Fig. 4(b)] shows a linear-shaped (inset semilog plot) graph, which indicates that a Mott-type insulator to conductor transition occurred after the device fabrication.³⁵ This may arise from the depletion of long-chain aliphatic ligands during the oxygen plasma and annealing process.³⁶ In Fig. 4(c), the p-type characteristic of the asprepared p-NW@QD can be readily identified from the gate voltage and source-drain current (V_g-I_{ds}) plots. P-Type nature is commonly observed in the solution-processed lead chalcogenide nanostructure, and its p-type characteristics are attributed to oxygen exposure and chemical interaction during device fabrication regardless of intrinsic stoichiometry.³⁷ According to the cylinder-on-plate model,³⁸ the maximum field-effect mobility was calculated to be 30.65 cm²/Vs, which is much higher than bulk PbS (5 cm²/Vs)³⁹ and other reported solution-processed analogous materials (Tables S1 and S2).^{22,40} We attribute this excellent hole mobility to the QD shells which boost the charge transport via manifold transfer dynamics (e.g., quantum tunneling or hopping).⁴¹ Indeed, various high mobility devices and our density functional theory (DFT) calculations (Figs. S8 and S9) revealed that long-range and fast charge transport could be realized in quantum confined nanostructures.⁴² In Figs. S8 and S9, through coating QDs on PbS nanowire surfaces, the calculated electron conductivity increased dramatically due to the improved tunneling charge transfer. The presence of QD allows electrons used to be free in three dimensions to be constrained in the orthogonal dimensions. While the dimension of the QD is comparable to the de Broglie wavelength of electrons, the generation of discrete energy values allows electrons to be able to tunnel through QDs. Also, the mobility value of PbS NW with a diameter of 46 nm $(26.6 \text{ cm}^2/\text{Vs})$ showed no significant difference from that of 83 nm PbS (Fig. S10 and Table S1). This is because the mean-free-path of charge carriers in PbS is much shorter than the currently measured NW diameter. Therefore, the carrier transport mechanism within p-NW@QD can be rationalized as (i) the thermal lattice scattering is dominated by acoustic phonons in the wire cores; (ii) the electronic transport is mainly contributed from the surface QD shells.

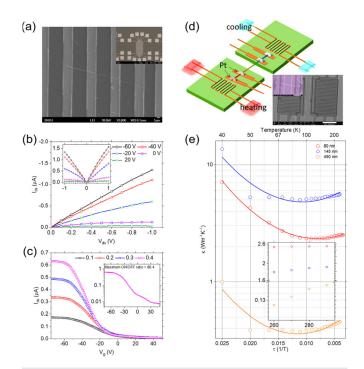


FIG. 4. (a) The SEM image of the fabricated back-gate FET. The inset shows the entire device morphology. The scale bars are 1 um and 100 um (inset), respectively. (b) Output I_{ds}-V_{ds} curves with gate voltages from -60 V to 20 V with a 20 V step. The inset shows the same plot on a log-scale. (c) Transfer I_{ds}-V_g curves at different applied V_{sd} conditions (0.1 V–0.4 V). The inset shows a log-scale I_{ds}-V_g curve at V_{sd} = 0.4 V for determining the device ON-OFF ratio. (d) Top: Schematic illustration of the microdevice consisting of two suspended heater pads bridged by a PbS p-NW@QD. Bottom: Scanning electron microscopy (SEM) image of the microfabric ated device for measuring thermal conductivity. The inset (high-resolution SEM) shows the detail configuration of the as-prepared microdevices. The scale bars are 10 um and 3 um (inset), respectively. (e) τ - κ Coordinate temperature dependence of thermal conductivity. The inset shows an enlarged graph within the temperature range of 250–300 °C.

To demonstrate the effect of the p-NW@QD core-shell structure on modulating thermal transport, we directly probed the heat flow kinetics through the state-of-the-art microfabricated suspended heatpad devices. The impacts of QD on the thermal conductivity have been theoretically perceived (Fig. S12) but have not been experimentally realized due to material preparation difficulties.^{7,16} As shown in Figs. 4(d), S13, and S14, drop-cast single PbS p-NW@QD was bridged on the device, and contact was made by finely depositing Pt through using a focused ion beam (FIB) system.⁴³⁻⁴⁵ Figures 4(e) and S15 show the thermal conductivity of the measured PbS p-NW@QD in the temperature range of 40 K-250 K. The peak of thermal conductivity appeared below 50 K and the thermal conductivity plateaus located above 50 K, which is relatively low compared to the previously reported silicacoated PbS NW.⁴⁰ This indicates that the phonon-phonon scattering is dominated by the U-process mechanism which is consistent with our conclusion derived from MD dynamical simulations. Moreover, as shown in the inset of Figs. 4(e) and S15, the as-prepared p-NW@QD does not exhibit a strong size-dependent effect in the current diameter range, which may be due to the fact that the PbS phonon mean-freepath is very short (<10 nm) and short-wavelength phonons can only

be affected by point defects or alloy scattering rather than boundary scattering. Therefore, the thermal conductivity was determined through averaging the "single-wire-bridging" thermal flow as a function of their diameters ($\bar{k} = 1.53 \pm 1 \text{ W/m K}$), which manifests an overall smaller thermal conductivity compared to the bulk values (Table S2).¹⁰

In conclusion, we have prepared quantum dotlike nanostructure coated core-shell anisotropic NWs through a solvent-mediated approach. We exemplified the p-NW@QD structure via PbS, which revealed that DER could be a sufficient coordinating reagent for tuning the morphology of NWs. Based on MD lattice dynamics simulations, we observed that the dimensional factor for efficient phonon scattering is limited below 10 nm, and we confirmed that the U-process is the governing mechanism of the photon scattering behavior in the lowdimension thermal transport dynamics. Through fabricating fieldeffect transistors and microfabricated suspended heating devices, we demonstrated an anomalously high electronic mobility (30.65 cm²/Vs) and low thermal conductivity $(1.53 \pm 1 \text{ W/m K})$. Direct charge/heat carrier flow measurements and computational simulations confirm that the anomalous electrical and thermal transport phenomenon is strongly dependent on the fast charge transport through the QD shell, and a slow phonon migration across the Umklapp-process dominated NW cores. Our findings could provide theoretical and experimental guidance for the future synthesis and design of nanostructures for controlling the charge and heat transport in thermoelectric as well as topological insulator studies. The solution-processed nature also indicates that the growth of these p-NW@QD structures could be scaled up for mass production. We expect the material innovation and fast developments in the field of large-scale nanostructure fabrication to enable high-performance thermoelectric materials to be realized in the near future.

See the supplementary material for detailed experimental procedures and supporting images (Figs. S1–S15) and Tables S1 and S2.

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