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# Heterogeneous Integration of Diamond

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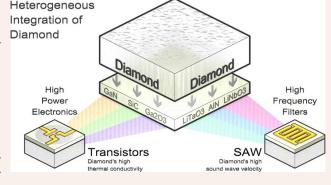


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CONSPECTUS: The heterogeneous integration of materials Heterogeneous offers new paradigms in many extreme applications, where single materials cannot solve the problem alone. Diamond has a plethora of superlative properties that make it attractive in a diverse array of applications, such as its unique combination of unrivalled thermal conductivity combined with high electrical impedance; single photon emission at room temperature; superlative acoustic wave velocity, and Debye temperature. Most of these properties are directly related to diamond's atomically dense lattice of light carbon atoms, which has consequences such as difficulty in doping diamond n-type and low thermal coefficient of thermal expansion. This last property presents a significant problem for the growth of diamond on nondiamond materials as the linear coefficients of



thermal expansion of other materials are often several times larger than diamond.

The integration of diamond with other materials is thus a complex and multifaceted problem, with various solutions with individual pros and cons. Our work focused on the growth of diamond directly on various materials or with enabling interlayers. In particular, we have focused on driving the self-assembly of diamond nanoparticles onto nondiamond surfaces as the precursors to diamond growth. This approach requires knowledge of the zeta potential as a function of pH of the surface to be treated as well as that of the diamond particles, and this Account provides a detailed summary of the most critical materials for diamond integration. The high density of nanodiamond particles as precursors for diamond growth is unfortunately not sufficient, and this Account discusses issues with adhesion and stress which are exacerbated by the aforementioned anomalously low thermal expansion coefficient. In this Account, we review this approach with references to competing approaches, detailing unavoidable issues such as wafer bow,

stresses, and the harsh environment of diamond chemical vapor deposition.

#### 1. INTRODUCTION

The extreme properties of diamond are well documented (Table 1) and exploited in a wide range of applications from oil and gas drilling to quantum technologies. However, these properties can be a double-edged sword. For example, the atomic density of diamond yields unrivaled hardness, thermal conductivity, Debye temperature, and sound wave velocity. But this same property makes diamond difficult to dope efficiently, with 1% activation of its most successful dopant (boron) at room temperature. Ntype doping is even more problematic with activations energies greater than the bandgap of germanium (0.6 eV). This doping limitation can be a significant advantage in quantum technologies, enabling the growth of very high purity materials with unrivalled coherence times.<sup>3</sup> The high Debye temperature of diamond helps room temperature single photon emission but can inhibit the transfer of heat across interfaces with other materials due to poor overlap of the phonon density of states. The anomalously low coefficient of thermal expansion of diamond can be adventitious for optical components such as lenses and mirrors but is an Achilles' heel when integrating with other materials due to the high temperature of diamond growth.

The integration of diamond with other materials is the focus of this Account. Heterogeneous Integration promises a combination of synergistic properties of dissimilar materials for specific applications where no individual material suffices. There are several obvious combinations from Table 1 such as the thermal management of Si, GaN or Ga<sub>2</sub>O<sub>3</sub> with diamond; <sup>5,6</sup> the combination of direct bandgap semiconductors such as GaN/ GaAs etc with diamond for pumping of its single photon centers and the integration of diamond with piezoelectric materials for Micro-Electro-Mechanical Systems (MEMS) and acoustic devices.<sup>7,8</sup> These combinations circumvent diamond's poor performance as a semiconductor (a consequence of its high atomic density); its lack of efficient light emission (a consequence of its indirect bandgap), and its lack of piezo-

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Table 1. Diamond Material Properties Comparison

	Si	SiC	GaN	$Ga_2O_3$	Diamond	Units
Bandgap	1.1	2.2-3.3	3.4	4.8	5.5	eV
Electron mobility	1400	900	1500	200	2000	$cm^2/(V s)$
Hole mobility	450	320	350	N/A	2000	$cm^2/(V s)$
Dielectric constant	11.7	9.7	8.9	10.2	5.7	-
Breakdown field	0.3	3	5	3.8	10	MV/cm
Thermal conductivity	1.3	1.2	1.3	0.3	22	$W/cm \cdot K$
Debye temperature	640	1200	600	800	2220	K
CTE (300 K)	2.5	4	3.2	1.8	1	ppm/K
Acoustic velocity	8000	12000	4400	3000-7000	20000	m/s
Substrate Ø	450	200	150	100	25 (300)	mm

electricity (a consequence of its elemental and isotropic lattice) respectively. Superlative performance has been demonstrated with such combinations as GaN/diamond HEMTS and AlN/diamond Surface Acoustic Wave devices.<sup>8</sup>

There are several routes to the Heterogeneous Integration of diamond such as the growth of diamond on the foreign material, the growth of the foreign material on diamond, and wafer bonding the two materials. There are advantages and disadvantages of each approach. For the case of diamond growth on other materials, the main advantage is the ability to use single crystal materials of the foreign material if they can withstand the high temperatures and harsh environment of diamond growth. The process is also wafer scale as diamond reactors are commercially available with over 12 in. diameter capability. Critical disadvantages are the polycrystalline nature of the diamond and large wafer bow/stresses due to differences in the coefficients of thermal expansion. Although diamond has been grown at temperatures down to 400 °C, the growth rate is not commercially viable for thicknesses beyond a few hundred nm and the quality degrades due to the nanocrystalline nature. <sup>10</sup> Thus, the growth of diamond on the foreign material can work well for thermal management of some materials such as GaN etc. but poorly for quantum applications due to the nature of the grown diamond. It is also attractive for devices such as MEMS or electrochemical electrodes, where bulk diamond is not necessary or even desirable. In the case of MEMS, the presence of a sacrificial underlayer is often essential, and diamond is not easily under etched.

For the growth of foreign material on diamond, lower temperatures may be possible depending on the material (for example, many piezoelectric materials can be sputtered at room temperature), leading to lower wafer bow and stress. Single crystal diamond could be used, which although is not quite wafer scale, sizes are increasing rapidly due to the synthetic gem market. The disadvantage is that few materials grow epitaxially on diamond; thus, it is the foreign material that is polycrystalline. This can work well for quantum technologies where the quality of the diamond is paramount, some photonic devices etc but the reduced quality of polycrystalline piezoelectrics can lead to undesirable insertion loss in Surface Acoustic Wave devices. <sup>11</sup>

Wafer bonding has recently shown promise in circumventing issues with both approaches above. There are multiple approaches including hydrophilic bonding, direct bonding, etc. <sup>12–15</sup> Surface Activated Wafer Bonding (SAWB) in particular provides a route to covalent bonds between potentially any materials with near atomically flat interfaces. <sup>16</sup> This is particularly important in the case of thermal management as heat transport through van der Waals bonded interfaces is limited. <sup>17</sup> In SAWB, wafers are irradiated with Ar ions under

ultrahigh vacuum to remove surface contaminants and termination. When the wafers are brought into contact with one another, the dangling bonds created at their surfaces heal to form covalent bonds between the two materials. As this is a room temperature technique, it does not result in high wafer bow and avoids the harsh environment of diamond growth. This enables the combination of many new materials hitherto impossible, with the main downside being the requirement of atomically flat surfaces and very low surface particle contamination. Thus, the Chemical Mechanical Planarization of diamond is critical. There is also a finite ( $\sim 1$  nm) amorphous interlayer between the diamond and foreign material  $^{20}$  which will have poor electrical properties and lead to states within the bandgap.

This Account focuses on the growth of diamond on nondiamond materials. For this approach to be successful, several conditions must be met. First, the material must be resistant to the harsh environment of diamond growth, both the growth temperature and also critically the dense atomic hydrogen plasma. In some cases, the in-diffusion of carbon species into the material may have negative consequences, such as for transistor channels. Second, the nucleation of diamond on the material must be possible, or an appropriate interlayer must be used to enable it. Third, the diamond must also adhere, preferably via a carbide bond, to withstand the thermal stresses generated by the mismatch in the coefficient of thermal expansion on cool down from chemical vapor deposition.

## 2. NUCLEATION/SEEDING

The growth of diamond on nondiamond substrates generally requires surface pretreatments due to a combination of the high surface energy of diamond, competition of nondiamond phases and the low sticking coefficient of the nucleation precursors. For an extensive review of the various approaches the reader is referred to a recent review, 21 for brevity here we focus on the most prevalent approach for initiating diamond growth on nondiamond substrates, namely the "seeding" with diamond particles. The term "nucleation" is not strictly speaking accurate in this context as the diamond particles act as "seeds" upon which diamond grows epitaxially. Early approaches involved mechanical abrasion with diamond grit or bombardment of surfaces with micrometer-sized particles via ultrasonic cavitation in colloids. It was later found that these methods leave diamond debris behind<sup>22</sup> and thus a logical evolution was to use the smallest diamonds available as seeds as this should lead to the highest nucleation density (by packing density). The smallest diamonds available (excepting the diamondoids which are volatile and poor nucleation sites 23) are those formed by detonation of high explosives such as TNT and RDX, often termed "Detonation Nanodiamond (DND) or Ultra-Dispersed

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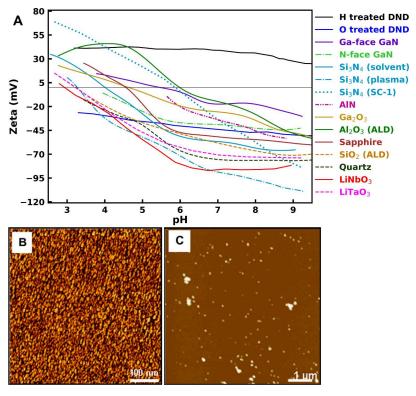


Figure 1. (A) Zeta potential as a function of pH for various high-power/high-frequency materials. Zeta potentials of hydrogen and oxygen treated nanodiamonds (DND) are also plotted in the same graph.  $^{5,29,39,40}$  The line at 0 mV was drawn for reference. (B) Atomic force micrograph of seeding with density approaching  $10^{12}$  cm<sup>-2</sup>. (C) Atomic force micrograph of seeding with density  $\sim 10^6$  cm<sup>-2</sup>. Reproduced with permission from ref 27. Copyright 2011 Elsevier.

Diamond (UDD).<sup>24</sup> Unfortunately, these 5 nm particles agglutinate into very large structures (>100 nm) which results in low nucleation densities. Efficient dispersion of these agglutinates took several decades from the initial discovery of the material but led to record breaking nucleation densities<sup>25</sup> and is now the most common approach to diamond seeding for thin films.

A critical difference between the earlier approaches with larger diamond particles and the dispersed 5 nm ones is their behavior in solution. Suspensions made with particles greater than one micron are prone to sedimentation due to gravity, whereas dispersed 5 nm solutions are true colloids with stability lasting several decades in water. This stability originates from the electrostatic repulsion of the particles from each other, a consequence of their net charge in solution or, more accurately "zeta potential". These charges originate from interactions between the surfaces of the diamond particles and the solvent. For example, diamond particles that have been annealed in air or purified in highly oxidizing solutions generally exhibit strong negative zeta potentials in water. This is because carboxyl (COOH) groups on the surface tend to become deprotonated, i.e., dissociate to COO which is negatively charged while the solvent, water in the case becomes more acidic (dues to increased H<sup>+</sup>). Of course, this dissociation is more prevalent at higher pH due to lower H<sup>+</sup> concentrations in the solvent; thus, zeta potentials are fundamentally pH dependent and should be quoted with the pH at which they were measured. A shorthand for this is the "isoelectric point", which is the pH at which the zeta potential is zero. At lower pH than the isoelectric point, the zeta potential will be positive, and at higher pH, negative. A secondary consequence of this proton donation is that colloids

of acid treated/oxidized diamond nanoparticles have increasingly acidic pH with increasing particle concentration whereas the opposite if true for positively charge particles.<sup>26</sup>

An application of this zeta potential is that particles can be attracted to or repelled from surfaces depending on the associated charge of the surface, and this should be considered when trying to create dense layers of nanoparticles. This also explains why early attempts to use detonation nanodiamonds as seeds were unsuccessful, as the materials were acid cleaned and thus of a negative zeta potential. Silicon surfaces also exhibit a negative zeta potential across the majority of the pH range and thus the like charges repel one another in solution leading to low nucleation densities.<sup>27</sup> In fact, the first demonstration of high nucleation densities was somewhat fortunate in that the milling process used to separate the agglutinates generated a positive zeta potential,<sup>28</sup> which drove self-assembly of the nanoparticles to the silicon surface. There are now several methods for producing positive<sup>29,26</sup> or negative zeta potential<sup>30,31</sup> diamond nanoparticles, and thus, if the zeta potential of the substrate is known, it is just a case of choosing the particle with the opposite

Figure 1 summarizes the zeta potentials as a function of pH for the substrates we have characterized to date, along with hydrogen and oxygen terminated nanodiamonds (DND) as the positive and negative seeds, respectively. It can be seen from this figure that the overwhelming majority of materials exhibit a negative zeta potential across the majority of the pH range (i.e., they have low isoelectric points, below pH 5) with the exception of the Ga face of GaN and  $Al_2O_3$  deposited by Atomic Layer Deposition (ALD). In the case of GaN, the Ga-face and N-face exhibit differing zeta potential behavior as a function of pH. The

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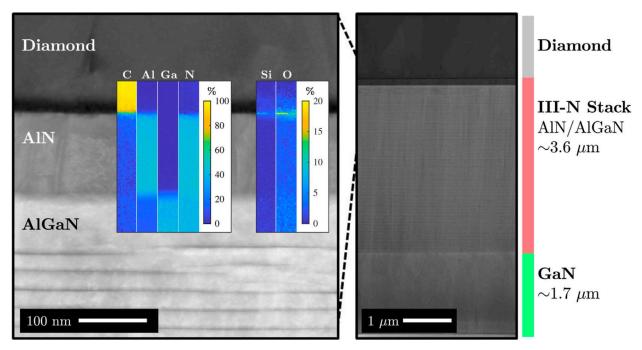


Figure 2. Cross-sectional HAADF-STEM micrograph and EDX data of CVD diamond grown on a  $0.5 \, \text{mm}$  GaN/III—N membrane. Reproduced with permission from ref 9. Copyright 2021 The Authors.

isoelectric point of the Ga-face is around pH 5.5, while that of the N-face is around pH 4. This difference is probably due to the higher oxygen adsorption ability of the N-face GaN, 32 the difference for seeding is however negligible. Sapphire and aluminum oxide grown by ALD also show small differences in isoelectric point (pH 4.5 vs pH 6 respectively) and are significantly different from aluminum oxide nanoparticles at pH 9.33 Gallium oxide shows similar effects with bulk single crystal isoelectric point around 5 whereas the nanoparticles of the same can be up to pH 9.34 Aluminum Nitride (AlN) is another highly complicated case, exhibiting both low (Figure 1, deposited by MOCVD) and high isoelectric points (nanoparticles)).35

AlN is complex due to the ease at which it oxidizes, and thus due to partial/complete oxidation, surfaces between different wafers can be drastically different. Thus, the structure of the material, as well as its surface termination, can influence the pH dependency of the zeta potential. Oxides such as LiNbO3, LiTaO<sub>3</sub>, SiO<sub>2</sub> (ALD), and quartz show much simpler behaviors, with low isoelectric points and little variation between wafers. The SiN surface exhibits a widely different isoelectric point after different cleaning steps. XPS studies revealed a direct correlation between low oxygen content on the surface and high iso electric point. While SC-1 cleaning was able to remove organic contaminants leading to low surface oxygen, oxygen plasma resulted in robustly oxygen terminated surfaces leading to quartzlike behavior. Full details of the cleaning procedure can be found in Bland et al.<sup>36</sup> From these results, it is clear that surface cleaning procedures have a profound effect on the surface termination/zeta potential and thus can influence the seeding of the substrates.

Figure 1B demonstrates the efficacy of this approach using opposite polarity zeta potentials for the substrate and seeds. In this case, the substrate is silicon and the seeds are hydrogen treated diamond nanoparticles. The final density is approaching the tip convolution limited resolution of the Atomic Force

Microscope at  $10^{12}$  cm<sup>-2</sup>. If the substrate and seed have identical polarity zeta potentials, then the density is 6 orders of magnitude lower (Figure 1C). It should be noted that other approaches can be used to manipulate the zeta potential of substrates such as the use of polymers of opposite polarity<sup>37</sup> and plasma treatments.<sup>38</sup>

#### 3. ADHESION AND SUBSTRATE EFFECTS

High densities of nuclei/seeds are a prerequisite for the growth of thin films of diamond on nondiamond substrates but are not sufficient. This is evident in cases such as GaN, Ga<sub>2</sub>O<sub>3</sub>, LiNbO<sub>3</sub>, and LaTaO3, where it is possible to generate very high seeding densities but after growth the films will delaminate on cooling to room temperature. This is due to a combination of effects, and each material needs to be considered independently; however, in the general case of seeding, clearly the diamond nanoparticles need to react with the substrate and form a stable carbide. The strength of this carbide bond needs to be able to withstand thermal stresses generated by the mismatch in thermal expansion coefficients. "High densities" are also very much within the eye of the beholder as the very abrupt interfaces generated by the highest density nanoseeding (>1011 cm-2) generally result in poorer adhesion for applications such as tooling where densities around  $<10^{10}$  cm<sup>-2</sup> provide a rougher and more mechanically robust interface. Many materials such as GaN, Ga<sub>2</sub>O<sub>3</sub>, LiNbO<sub>3</sub>, LaTaO<sub>3</sub>, etc. are also vulnerable to atomic hydrogen and in extreme cases will leach liquid gallium into the plasma.<sup>5</sup> The vapor pressure of substrates such as Fe, Co, Ni, Cr, etc. can also interfere with the initial stages of

A common approach to mitigating the above issues is to use an interlayer between the substrate and the diamond. SiN interlayers have been demonstrated to enable nucleation on GaN with great success, the result of which is now commercialized. AlN or AlGaN is also a good option between GaN and diamond for enhancing adhesion, and it can be grown epitaxially on GaN. Figure 2 shows an example of an AlN/

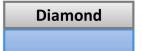
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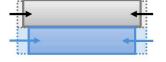
Table 2. Thermal Stress Mismatch Estimations with a Diamond

	Si	SiC	GaN	$Ga_2O_3$	Diamond	Units	ref
Young's Modulus	168	392	311	261	1000	GPa	9, 44-46
Change in CTE $\alpha$ (25 °C)- $\alpha$ (800 °C)	<b>-1.</b> 7	<b>-2.</b> 7	-1.76	<b>-4.3</b>	-3.5	ppm K <sup>-1</sup>	43, 47
Tensile Strength	0.7	>2	>4	>7	>4	GPa	48-54
Thin Film Stress on a Diamond Substrate <sup>a</sup>	0.24	0.26	0.43	-0.15	-	GPa	
		_					

<sup>a</sup>Temperature dependent CTE over a CVD range  $\Delta T = 800-25$  °C.

Substrate







**800** °C

**800** °C

Substrate heated in CVD diamond growth CVD chamber > 24h

**25** °C

Cool to room temperature (Thermal stress)

**25** °C Stress differential ↓

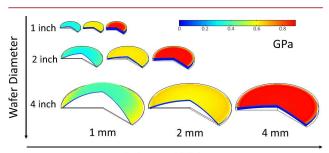
Wafer Bow

Figure 3. Diamond film on the substrate.

AlGaN stack between GaN and diamond membrane, exhibiting a highly abrupt interface and strong adhesion. This High-angle annular dark-field (HAADF) image from Scanning Transmission electron microscopy (STEM) shows a thick carbon rich (diamond) layer bound to an aluminum and nitrogen rich (AlN) layer. There is also a very thin layer of silicon and oxygen which originates from the silicon handle structure beneath the GaN.

#### 4. THERMAL STRESS

One of the greatest challenges of direct heteroepitaxial growth of diamond using CVD on any semiconducting substrate is the



Substrate Thickness

**Figure 4.** Axisymmetric finite element model of von Mises stresses with cross sections and exaggerated displacement fields ( $\times$ 50) of 100  $\mu$ m diamond on Si cooled to room temperature from 800 °C using CTE data from, <sup>43</sup> deformation scale exaggerated by arbitrary value for visibility.

high stresses incurred. Since CVD typically occurs at high temperatures, thermal stresses occur owing to the differing coefficients of thermal expansion (CTE) of diamond and the materials in the substrate. The materials are bound at the interface but have differing strain profiles upon cooling, resulting in either tensile or compressive stresses in the layer. In general, the thermal stress  $\sigma$  in 1D is given by

$$\sigma(T) = E_{\text{layer}} \Delta T \Delta \alpha(T)$$

where  $E_{\rm layer}$  is Young's modulus, T is the temperature, and  $\alpha$  is the linear CTE. Large thermal stresses in the layers incur owing to a high Young's modulus, high diamond deposition temperatures (in excess of 800 °C), and diamond's low CTE compared to other materials. <sup>43</sup> As such, diamond in most cases is held in compression, while the other layers are in tension. This formula provides a general estimate of the interfacial stresses between the materials and whether the deposition will survive the CVD diamond process. Table 2 collates literature data and provides some general calculations of the thermal stress mismatch on diamond at ( $\Delta T = 800-25$  °C).

It is important to note that these simplified calculations assume that a thick layer of diamond is deposited onto a freestanding thin film. These results provide some insight into the magnitude of stress that the material will undergo during the CVD thermal cycling process. Since the materials have varying CTE's the difference in strain results in "wafer bow" as is shown in Figure 3. This is because the materials are bound at the interface in the 1D radial direction, and thus, the thermal stress can only be relieved through 2D out-of-plane wafer deformations. For cylindrical 2-layer systems, the magnitude of this bow is described by the Stoney formula through the radius of curvature and the biaxial modulus: <sup>55</sup>

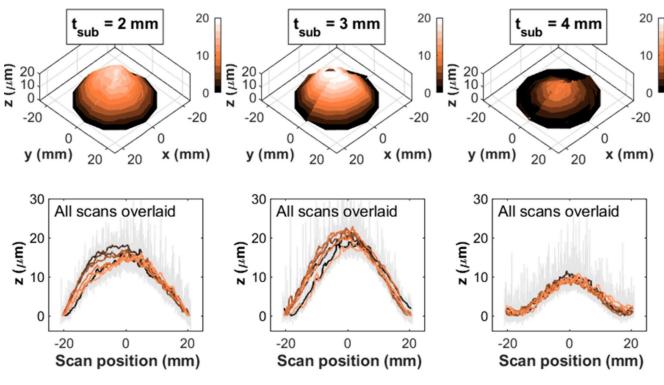
$$R = E' \frac{t_{\text{sub}}^2}{t_{\text{film}}} \frac{1}{6} \frac{1}{\sigma_{\text{total}}}$$

where "sub" and "film" denote the substrate and film properties, respectively,  $E' = \left[\frac{E}{1-\nu}\right]$  is the biaxial modulus,  $\nu$  is the Poisson's ratio, t denotes the thickness, and  $\sigma_{\rm total}$  denotes the total stress in the film. The central bow can then be estimated:

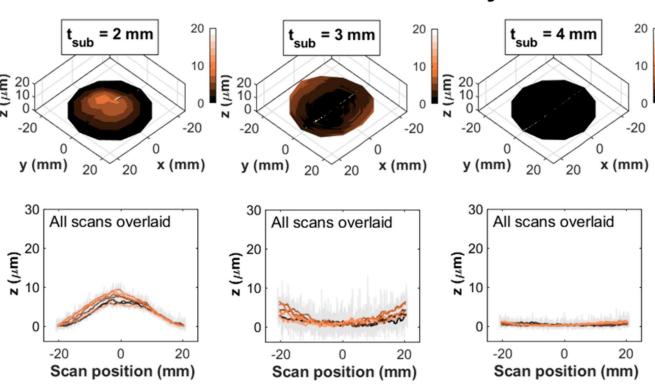
$$w = R \left( 1 - \cos \left[ \frac{r_{\text{sub}}}{R} \right] \right)$$

where r is the radius of the substrate. Larger wafers suffer from significant bowing, ultimately making any subsequent device processing difficult. Thus, to minimize the bow (maximize R) for

# **Top Side Profilometry**



# **Bottom Side Profilometry**



**Figure 5.** Surface profilometry of thick diamond films on 2" Si wafers of varying substrate thickness; top side radial profilometry data from ref 57 with additional bottom side data demonstrating reduced bow.

larger wafers, it is clear that the substrate thickness of the substrate must be increased.

Here, one of the biggest challenges of diamond scalability is highlighted, where impractically thick substrates are the only way to sufficiently reduce the bow for high quality diamond growth on large wafers. Taking simple diamond on Si models from previous work has shown that even at 1 mm substrate thicknesses of silicon, the bow is still several hundred microns. <sup>56</sup> Going to much thicker substrates, a simple thermal stress finite element model of diamond on Si is shown in Figure 4. These simple modeling results clearly show the effect of substrate thickness and wafer diameter on the relative deformation and residual stresses, von Mises stress; also note that the computed von Mises residual stresses in FEM solutions are generally different to the simple 1D thermal stress model. Although less practical for the large wafer scale, the substrate thickness effect was experimentally shown for 40-46  $\mu$ m thick (by mass) diamond growth on 2 in. Si wafers as shown in Figure 5,57 where the seemingly bowed top surface profilometry was actually a thickness variation since the Si underside was actually relatively flat in comparison. In this work, the authors demonstrate that both spatial variation and bow over 2 in. are best eliminated with 4 mm thick Si.

To reduce bowing and thermal stress as a whole, the growth temperature can be reduced; however, this results in lower quality diamond and much lower growth rates owing to the reduced H radical density at lower power and pressure. Thus, assuming the materials can survive such stress, prestressing wafers in the opposite direction to counteract wafer bow is a potential solution for thin substrates.

### 5. CONCLUSION

The last two decades have seen substantial advances in the heterogeneous integration of diamond. The conditions that must be met for the successful growth of diamond on a nondiamond material are mostly known, however as we have tried to demonstrate above, they are multifaceted and often not all met in their entirety. For example, being able to nucleate and grow thin films of diamond on a material is not evidence of being able to grow the 10-100s of microns on it required for efficient thermal management. The anomalously low coefficient of thermal expansion of diamond creates real challenges as the growth temperature of diamond at commercially relevant growth rates is high (>700C). This is a mostly unavoidable physical constraint, and new approaches such as room temperature Surface Activated Wafer Bonding provide an exciting circumvention of this critical issue. Direct diamond growth on materials (or via an enabling interlayer) is still highly relevant for materials such as silicon, some III-Vs, metals, etc.; however, materials that do not form a stable carbide, have high thermal expansion coefficients, or are vulnerable to hydrogen plasmas are likely best served by wafer bonding approaches that can integrate the materials after their isolated growth processes.

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#### **Author Contributions**

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#### Notes

The authors declare no competing financial interest.

#### **Biographies**

Oliver A. Williams received his BEng in 1998 and PhD in 2003, both from University College London. He then worked as a Center for Nanoscale Materials Distinguished Postdoc in the Nanocarbon theme at Argonne National Laboratory between 2003/4. Following this he moved to IMO/IMEC in Hasselt University Belgium where he worked as a Research Scientist for four years. He then accepted an "Attract" award at the Fraunhofer Institute for Applied Solid State Research in Freiburg, Germany where he was team leader of Diamond Technology for three years. He is now a Professor in Experimental Physics at Cardiff School of Physics and Astronomy. His research group, Cardiff Diamond Foundry, focuses on diamond growth and technology towards SAW, MEMS, Thermal Management, nanoparticles, superconductivity and quantum technologies.

Soumen Mandal completed his PhD in experimental condensed matter physics as part of the MSc-PhD Dual Degree program at the Indian Institute of Technology Kanpur in 2009. Following his doctoral studies, he undertook a postdoctoral research position at Institut Neel in Grenoble, France. In 2013, he joined Cardiff University in the United Kingdom as a Research Associate. Dr. Mandal's research focuses on spintronics, thin magnetic films, superconductor-ferromagnet multilayers, superconducting diamond devices, diamond thin film growth, and diamond nanoparticles. Currently, his research interests include diamond-based devices, diamond thin film nucleation and growth, and diamond nanoparticles.

Jerome A.Cuenca completed his PhD in microwave engineering and materials science at the Centre for High Frequency Engineering at Cardiff University in 2015. He joined the Cardiff Diamond Foundry as a Research Associate in 2017. His primary research focuses are on microwave dielectric spectroscopy systems for carbonaceous materials, microwave applications of diamond, microwave plasma chemical vapour deposition of diamond and the optimization of plasma reactors using finite element modelling.

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**Accounts of Materials Research** 

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